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(Acts whose publication is obligatory)

COUNCIL REGULATION (EC) No 1556/95

of 29 June 1995

temporarily suspending the autonomous Common Customs Tariff duty on certain industrial products (in the microelectronics and related sectors)

THE COUNCIL OF THE EUROPEAN UNION,

Having regard to the Treaty establishing the European Community, and in particular Article 28 thereof,

Having regard to the proposal from the Commission,

Whereas production of the products referred to in this Regulation is at present inadequate or non-existent within the Community and producers are thus unable to meet the needs of user industries in the Community;

Whereas it is in the Community's interest in certain cases to suspend the autonomous Common Customs Tariff duties only partially, particularly because of the existence of Community production, and in other cases to suspend them completely;

Whereas suspension of these autonomous duties shall be decided by the Community;

Whereas, taking account of the difficulties involved in accurately assessing the development of the economic situation in the sectors concerned in the near future, these suspension measures should be taken only temporarily, by fixing their period of validity by reference to the interests of Community production,

HAS ADOPTED THIS REGULATION:

Article 1

The autonomous Common Customs Tariff duties for the products listed in the tables appearing in the Annex shall be suspended at the level indicated in respect of each of them.

These suspensions shall apply:

— from 1 July to 31 December 1995 for the products referred to in Table I,

— from 1 July 1995 to 30 June 1996 for the products referred to in Table II.

Article 2

This Regulation shall enter into force on 1 July 1995.

This Regulation shall be binding in its entirety and directly applicable in all Member States.

Done at Luxembourg, 29 June 1995.

For the Council

The President

J. BARROT

ANNEX

TABLE I

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8471 93 51	*30	Drive-unit only for reading optical CD-ROM discs of the 5,25 inch type, for use in the manufacture of products falling within heading 8471 (a)	0
ex 8473 30 10	*60	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, in the form of a monolithic integrated circuit, contained in a housing mounted on a printed circuit the exterior dimensions of which do not exceed 60 x 60 mm, and with decoupling capacitors, and bearing: — an identification marking consisting of or including one of the following combinations of figures: 486 80386 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*65	Memory module, consisting of at least 2 dual port dynamic random-access memories (D-RAMs) with a total storage capacity of 2 Mbits or more, with data registers and a serial read output control, in the form of monolithic integrated circuits mounted on a printed circuit, comprising passive components	0
ex 8473 30 10	*70	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, coomprising a bus controller and a memory controller, in the form of a monolithic integrated circuit, contained in a housing the exterior dimensions of which do not exceed 48 x 48 mm, and with decoupling capacitors, and bearing: — an identification marking consisting of or including the following combination of figures and letters: 390 Z 50 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*75	Memory module, consisting of at least 8 dynamic random-access memories (D-RAMs) with a total storage capacity of 4 Mbits or more and a static random-access cache memory (S-Cache-RAM) mounted on a printed circuit	0
ex 8473 30 90	*25	Pointing device (so-called 'trackball'), consisting of printed circuit on which are mounted an optical encoder in the form of a monolithic integrated circuit and a housing comprising a ball and a retainer ring, for use in the manufacture of products falling within subheading 8471 20 20 (a)	0
ex 8473 30 90	*85	Read/write assembly for hard disc storage units, comprising only one magnetic head of thin-film technology mounted on a carrier arm, capable of reading/writing to a density of 78 tracks or more per mm	0
ex 8501 10 99	*73	DC motor, brushless, with a typical running torque not exceeding 0,0024 Nm, with a coupling flange of a diameter of 32 mm (±0,5 mm), a chuck of a diameter of 25 mm (±0,5 mm), a rated speed of 3 663, 4 500 or 5 400 (±0,5 %) rpm and a supply voltage of 12 V (±10 %), integrated on an aluminium baseplate the exterior dimensions of which do not exceed 102 x 147 mm	0
8504 90 11		Ferrite cores	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8517 82 90	*30	Transmit unit, capable of converting electrical signals into light pulses, operating at a wavelength of 1 310 nm (±20 nm), comprising a laser diode, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: FLD130F3ACH or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 92	*20	Assembly comprising light-emitting diodes (LEDs)	0
ex 8517 90 98	*03	Assembly comprising light-emitting diodes (LEDs) and a lens	0
ex 8517 90 98	*04	Parts, for use in the manufacture of products falling within subheading 8517 82 10 (a)	0
ex 8522 90 91	*92	Electronic assembly for a laser read-head of a compact disc player, comprising: — a flexible printed circuit, — a photo-detector, in the form of a monolithic integrated circuit, contained in a housing, — not more than 2 connectors, — not more than 1 transistor, — not more than 3 variable and 4 fixed resistors, — not more than 5 capacitors, the whole mounted on a support	0
ex 8522 90 98	*31	Thin-film recording and reproducing device, having 9 parallel channels for digital signals and 2 channels for analogue signals, to which a non-magnetic ceramic substrate is fixed, the whole rounded at one side, for use in the manufacture of magnetic heads for digital sound recording and digital/analogue sound reproducing apparatus of the cassette-type (a)	0
ex 8522 90 98	*32	Sound reproducing assembly, consisting of a compact disc mechanism, comprising an optical reading system and 3 DC motors, for use in the manufacture of products falling within subheading 8527 21 10 (a)	0
ex 8529 10 70	*50	Dielectric filter for centre frequencies of 902,5 and 947,5 MHz, with a bandwidth of at least 25 MHz, contained in a housing	0
ex 8529 10 70	*55	Dielectric filter for a centre frequency of 947,5 MHz, with a bandwidth of at least 25 MHz, contained in a housing	0
ex 8529 10 70	*60	Dielectric filter for centre frequencies of 1 747,5 and 1 842,5 MHz, with a bandwidth of at least 75 MHz, contained in a housing	0
ex 8529 10 70	*65	Dielectric filter for a centre frequency of 1 842,5 MHz, with a bandwidth of at least 75 MHz, contained in a housing	0
ex 8529 10 70	*70	Dielectric filter for a centre frequency of 1 747,5 MHz, with a bandwidth of at least 75 MHz, contained in a housing	0
ex 8531 20 51	*10	Liquid crystal colour display (LCD) with an active matrix and 640 × 480 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8531 20 51	*20	Liquid crystal colour display (LCD) with an active matrix and 768 × 1 024 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, comprising electronic components providing drive and/or control functions	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8531 20 59	*10	Liquid crystal monochrome display (LCD) with an active matrix and 640 x 400 or 640 x 480 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, mounted on a printed circuit board comprising electronic components providing drive and/or control functions	0
ex 8531 20 59	*20	Liquid crystal monochrome display (LCD) with an active matrix and 900 x 1 152 pixels, consisting of a layer of liquid crystals between two glass sheets or plates, comprising electronic components providing drive and/or control functions	0
ex 8531 20 80	*10	Liquid crystal display (LCD) with a passive matrix, comprising electronic components providing drive and/or control functions	0
ex 8533 40 10	*91	Potentiometer consisting of carbon resistors with a total resistance of 20 KOhm or more but not exceeding 250 KOhm, fitted with a DC motor having an operating current of 150 mA at 4,5 V	0
ex 8534 00 19	*92	Single-face printed circuits, each with not more than 268 conductive leads, on a plastic tape with sprocket holes on both edges and having a width of not more than 35 mm and a thickness of not more than 0,26 mm	0
ex 8534 00 19	*94	Printed circuit, consisting of 31 conductor elements fixed on a flexible plastic film, for use in the manufacture of magnetic heads for digital sound recording and digital/analogue sound reproducing apparatus of the cassette-type (a)	0
ex 8536 50 90	*92	Reed switch having a switching power of 20 W or more within the range of 17–43 A.turn, in the form of a glass capsule, not containing mercury, the dimensions of which do not exceed 3 x 21 mm, for use in the manufacture of automotive airbag shock-sensors (a)	2
ex 8540 12 00	*81	Flat screen monochrome cathode-ray tube with a diagonal measurement of the screen of 150 mm or more but not exceeding 155 mm and an anode voltage of 25 kV or more but not exceeding 32 kV	0
ex 8541 10 91	*10	Silicon power rectifier diodes of planar technology, with a recovery time of less than 100 ns, a maximum recurring reverse voltage of 200 V, and average forward current of 2,5 A or more, contained in a housing	0
ex 8541 10 91	*20	Silicon power rectifier diode, with a reverse peak voltage not exceeding 1 500 V and an average output current of 8 A, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: PG151S15 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 40 19	*40	Light-emitting diode (LED), operating at a nominal wavelength of 567 or 700 nanometers, contained in a housing of the SMD (Surface mounted device) type	0
ex 8541 60 00	*94	Piezo-electric crystal, excluding surface acoustic wave filters, oscillating at centre frequency of 450 kHz or more but not exceeding 1 843 MHz	0
ex 8541 90 00 ex 8542 90 00	*10 *20	Housing or ceramic substrate, with connections	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 01	*60	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with a processing capacity of 4 bits, for use in the manufacture of goods of subheading 8542 11 51 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 7507 75108 75217 75312 75516 75P216 7508 75112 75304 75316 75617 75P308 75004 75116 75306 75352 75P008 75P316 75006 75216 75308 75512 75P116 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 12 ex 8542 11 14 ex 8542 11 16 ex 8542 11 18	*14 *04 *09 *04	Random-access memory, with separate in- and outputs and serial shift registers (so-called field memories), of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 521 000 TMS 4C2070 TMS 53805 TMS 4C1081 TMS 4C2970 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 16	*03	Dual port dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a serial read output control, with a storage capacity of 2 Mbits or more but not exceeding 4 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HM 538253 MT 42C256 PD 482234 TC 528257 HM 5316123 MT 42C8254 PD 482235 TC 528267 M5M482256 MT 42C8255 TC 524162 TMS 55160 MB 818251 MT 42C8256 TC 524165 TMS 55165 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 18	*05	Dynamic synchronous random-access memory of C-MOS technology (C-MOS synchronous D-RAM), with a storage capacity of 16 Mbits and an access time not exceeding 38 ns, operating with a supply voltage of 3,3 V (±0,3 V), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 626402 TMS 626802 TMS 636402 TMS 636802 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*14	Static random-access cache memory of BiMOS technology (BiMOS S-Cache-RAM), with a storage capacity of 64 Kbits and an access time not exceeding 20 ns, comprising an 8-bit address comparator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: IDT 71B74 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21	*15	Static random-access memory of MOS technology combined with ECL technology, with a storage capacity of 64 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 10C494-15 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*22	Static asynchronous random-access memory of C-MOS technology (C-MOS asynchronous S-RAM), with a storage capacity of 16 K x 16 bits, excluding static random-access cache memory (S-Cache-RAM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MCM 62995-17 MCM 62995-20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*10	Static random-access memory (S-RAM), with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXK581 000 CY7C108 HM 624257 MCM 6729 CXK581020 CY7C109 HM 628128 MT 5C1008 CY7C101 EDI 88128 KM 681 000 TC 551001 CY7C102 GM 76C8128 M5M 51004 TC 55B4256 CY7C106 HM 621100A M5M 51008 TC 55B4257 CY7C107 HM 624256 MCM 6228 TC 55B8128 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*13	Static random-access memory of C-MOS technology (C-MOS S-RAM), operating at a power supply of 3,3 V (±0,3 V), with a storage capacity of 288 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CY7C1388 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 27	*04	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 32 K x 36 bits, 64 K x 18 bits or 128 K x 9 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXK 77910 CY7C1031 CY7C1032 MT 58LC32 MT 58LC64 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 27	*06	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 4 Mbits or more, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 554002 MT5C512K8B2 MT5LC512K8D4 MT5C1M4B2 MT5LC1M4D4 MT5C256K16B2 MT5LC256K16D4 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 34 ex 8542 11 41	*45 *45	UV erasable or non-erasable, programmable, read only memory (EPROM or PROM) with a storage capacity of 1 Mbit and an access time not exceeding 45 ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, bearing : — an identification marking consisting of or including one of the following combinations of figures and letters: CY27H010-25 CY27H010-35 CY7B201 CY7B211 CY27H010-30 CY27H010-45 CY7B210 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*25	Electrically erasable, programmable, read only memory (E^2PROM), with a storage capacity of 8 K × 8 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 C 64 28 H 64 28 HC 64 28 PC 64 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*41	Electrically erasable, programmable, read only memory (E^2PROM), with a storage capacity of 256 Kbits or more, excluding flash electrically erasable, programmable, read only memory (Flash-E^2PROM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 28256 28 C 512 AT 28C1 024 28 C 256 48 C 256 E/M 28C010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*83	Flash electrically erasable, programmable, read only memory (Flash-E^2PROM) with a storage capacity of 1 Mbit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 F 001 28 F 210 29 F 100 28 F 010 29 F 010 48 F 010 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 38	*85	Flash electrically erasable, programmable, read only memory (Flash–E²PROM) with a storage capacity of 2 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 F 002 28 F 020 28 F 200 29 F 200 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*87	Flash electrically erasable, programmable, read only memory (Flash–E²PROM) with a storage capacity of 4 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 28 F 004 28 F 400 29 F 040 28 F 040 29 C 040 TC 584 000 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*89	Flash electrically erasable, programmable, read only memory (Flash–E²PROM) with a storage capacity of 8 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 28 F 008 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*91	Flash electrically erasable, programmable, read only memory (Flash–E²PROM) with a storage capacity of 16 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 28 F 016SA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*24	FIFO (first in, first out) read/write memory of C–MOS technology, capable of asynchronous reading and writing, with a storage capacity of 256 × 9 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CY7C419 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*33	FIFO (first in, first out) read/write memory of C–MOS technology, capable of asynchronous reading and writing, with a storage capacity of 512 × 9 bits, 1 K × 9 bits, 2 K × 9 bits or 4 K × 9 bits and an access time not exceeding 15 ns, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 40 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: Am 7204A-15 CY7C425 CY7C433 CY7C421 CY7C429 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*01	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a data memory, a programme memory and a display control or drive circuit, in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 83C751 CXP 82320 M 3817 MB 89098 83C752 CXP 85228 M 38203E4 PD 75316 87C750 CXP 85232 M 38203M2 TMP 87CC20F 87C751 CXP 85340 M 38207E8 TMP 87CH20F 87C752 M 37500M5 M 38207M8 TMP 87CK70AF CXP 82316 M 37500M8 M 3825 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*02	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, comprising a data memory with a storage capacity not exceeding 9 Kbits, a programme memory with a storage capacity not exceeding 256 Kbits, a serial synchronous communication interface consisting of an 8-bit serial shift register with serial data input, serial data output and serial shift clock, and in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: COP 820 COP 881C COP 888CG MB 89152 COP 840 COP 884CF COP 888EG MB 89P657A COP 880C COP 888CF MB 89145 MB 89W147 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*04	Microcontroller or microcomputer of C-MOS-technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) with a storage capacity of 16,5 Kbits and a random-access memory (RAM) with a storage capacity of 1 Kbit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 76032KC or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*49	Microcontroller or microcomputer of C-MOS or N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 32 Kbits or more but not exceeding 256 Kbits and one or more random-access memories (RAMs) with a total storage capacity not exceeding 8 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: 7742 87C504 CXD 80724 MC68HC05i8 80C51 87C51 CXP 80524 MC68HC705i8 80C52 87C52 M 37450E8 MN 1871215 83C055 87C54 M 37450M8 PCA 84C640 83C504 87C58 M 38063M6 PCA 84C840 83C51 87L51 M 38063E8 PCA 84C841 83L51 C 1900 M 38067M8 PD 78014 8751 C 2900 M50958 PD 78064 87C055 C 3900 M50959 PD 78134 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*51	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, comprising a random–access memory (RAM) with a storage capacity not exceeding 2 Kbits, a programmable, non–erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 64 Kbits or a flash electrically erasable, programmable, read only memory (Flash–E²PROM) or a read only memory, non–programmable (ROM) with a storage capacity of 32 or 64 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 77 C 82 AT 89C51 M 50747 80 C 152 M 50743 MC 68HC11A8 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*53	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, comprising a read only memory, non–programmable (ROM) with a storage capacity of 256, 320 or 384 Kbits and a random–access memory (RAM) with a storage capacity of 10 496 or 11 008 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXP 87132 CXP 87140 CXP 87240 CXP 87248 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*73	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, comprising a random–access memory (RAM) with a storage capacity of 4 or 6 Kbits, a read only memory, non–programmable (ROM) or a programmable, non–erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM), with a storage capacity of 96 or more but not exceeding 192 Kbits, an electrically erasable programmable, read only memory (E²PROM) with a storage capacity of 4 or 5 Kbits and an 8–channel analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: MC 68HC11E9 MC 68HC11L6 MC 68HC711K4FN MC 68HC11K4 MC 68HC711E9 MC 68HC711K4FS or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*01	Microcontroller or microcomputer of N–MOS technology (including H–MOS), with a processing capacity of 16 bits, comprising at least one read only memory, non–programmable (ROM) with a storage capacity of 510 x 13 bits or an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 512 x 13 bits, a random–access memory (RAM) with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PD 7720 PD 77 P 20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*02	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, comprising a random–access memory (RAM) with a storage capacity of 48 Kbits and having the function of programme memory, a random–access memory (RAM) with a storage capacity of 32 Kbits, a digital–to–analogue converter and an analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 21msp58 ADSP 21msp59 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*05	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits and a 16–bit address–bus and an 8–bit data–bus, comprising a random–access memory (RAM) with a storage capacity of 4 Kbits or more, a read only memory, non–programmable (ROM) or a programmable non–erasable read only memory (PROM) or a UV–erasable, programmable, read only memory (EPROM) with a storage capacity of 128 Kbits or more, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MB 89715 MB 89P715 MB 89W715 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 53	*07	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising sixteen 8-bit or eight 16-bit registers, a read only memory, non-programmable (ROM) or a programmable, read only memory (PROM), with a storage capacity of 128 Kbits, a random-access memory (RAM) with a storage capacity of 4 Kbits, 3 timers, a serial communications interface, an 8-channel analogue-to-digital converter and 9 input/output ports, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HD 6473308CP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*09	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising an 8-bit or 16-bit external data-bus, a random-access memory (RAM) with a storage capacity of 16 Kbits and having the function of programme memory, a random-access memory (RAM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DSP 56116 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*15	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, for controlling dataflows between a twisted-pair-cable local area network (LAN) and a central processing unit (CPU), comprising a read only memory, non-programmable (ROM) with a storage capacity not exceeding 32 Kbits, a random-access memory (RAM) with a storage capacity not exceeding 128 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SMC 83C825 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*17	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, providing local network bus communication control, comprising a random-access memory (RAM) with a storage capacity of 2 Kbits, 2 read only memories, non-programmable (ROMs) or 2 programmable, non-erasable, read only memories (PROMs) or 2 UV erasable, programmable, read only memories (EPROMs) with a total storage capacity of 56 or 128 Kbits and a serial-port interface circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 8370C03 TMS 8370C73 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 53	*21	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, having the function of charge control of nickel–cadmium batteries, comprising a read only memory, non–programmable (ROM) with a storage capacity of 42 000 bits, a read only memory, non–programmable (ROM) with a storage capacity of 1 Kbit, a random–access memory (RAM) with a storage capacity of 1 Kbit and a 10–bit analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: ICS 1 700 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*29	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, comprising of a read only memory, non–programmable (ROM) with a storage capacity of 64 Kbits, a random–access memory (RAM) with a storage capacity of 32 Kbits and a static random–access cache memory (S–Cache–RAM) with a storage capacity of 15 x 16 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DSP16A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*31	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non–programmable (ROM) or a programmable, non–erasable, read only memory (PROM) with a storage capacity of 64 or 128 Kbits, a random–access memory (RAM) with a storage capacity of 2 Kbits, an 8–channel analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PD 78C12AG PD 78C14 PD 78CP14G or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*33	Microcontroller or microcomputer with a processing capacity of 16 bits, comprising a read only memory, non–programmable (ROM) or a programmable, non–erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM) with a storage capacity not exceeding 256 Kbits, a random–access memory (RAM) with a storage capacity not exceeding 16 Kbits, an analogue–to–digital converter with sample/hold, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 53 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8396 83C196 H8/532 8397 83C198 HD 6435368 8796 87C196 HD 6475368 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*37	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128, 192, 256, 384 or 480 Kbits, a random-access memory (RAM) with a storage capacity of 4, 8 or 16 Kbits and an 8-bit analogue-to-digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M 37702 E2 M 37702 M2 M 37702 M4 M 37702 E4 M 37702 M8 M 37702 M6L M 37702 E8 M 37702 M3B M 37702 MDB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*41	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a multiplier/accumulator (MAC), an arithmetic-logic shifter, a microprocessor interface port, a read only memory (ROM) with a storage capacity of 48 Kbits, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits, an analogue-to-digtal converter, a digital-to-analogue converter and a programmable timer, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 21msp52BS-52 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*43	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 16 bits, comprising a multiplier/accumulator (MAC), an arithmetic-logic shifter, a static random-access memory (S-RAM) with a storage capacity of 48 Kbits and having the function of programme memory, a static random-access memory (S-RAM) with a storage capacity of 16 Kbits and a programmable timer, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 2101 ADSP 2102BS-50 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 53	*53	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, comprising a static random–access memory (S–RAM) with a storage capacity of 8 Mbits and having the function of programme memory, a static random–access memory (S–RAM) with a storage capacity of 8 Kbits, a system integration module (SIM), an anlogue–to–digital converter a timer, 2 serial interface circuits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 68HC16Z1 (SC415902FV) or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 53	*54	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 16 bits, comprising a programmable, read only memory (PROM) with a storage capacity of 48 Kbits and a random–access memory (RAM) with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 77P25 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*06	Bus controller of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 2782654 82 C 101 82 C 496 HT 321 69G1705 82 C 103 82 C 591 L1A 4601 82303 82 C 211 82 C 597 MSM 6307 82304 82 C 288 82 C 801B R 4220 82306 82 C 301 82 C 802G R 4230 82308 82 C 320 82 C 822 TACT 83443 82309 82 C 362 82 C 88 VAC 068 82355 82 C 461 CA 91C014 VIC 068 82358 82 C 463 ET 6 000 VIC 64 82374EB 82 C 465 GC 181 VL 82 C 331 82434LX 82 C 493 HT 216 VY 86 C 410 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*09	Video controller, with at least one of the following functions: a) cathode–ray tube controlling, b) liquid crystal display (LCD) driving or controlling, c) graphics controlling, d) colour selection controlling, in the form of a monolithic integrated circuit, either contained in a housing or fixed on a plastic support, and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: a) 82 C 434 b) HD 44100 b) V 6116 a) 82 C 453 b) HD 44780 b) V 6117 a) 86 C 805 b) HD 66100 b) V 6118 a) 86 C 911 b) HD 61104T b) V 6120 a) 86 C 928 b) HD 61105T b) V 6355-DJ a) AM 8052 b) HD 66106T b) WD 90C24 a) ATI 68800 b) HD 66107T c) 82 C 431 a) CL-GD542 b) LC 7582 c) 82 C 435 a) CL-GD543 b) M 6003 c) 82 C 441 a) CRT 9007 b) M 6004 c) 82 C 451 a) CRT 97 C 11 b) MSM 5259 c) 82 C 452 a) CXA 1839 b) MSM 5298 c) 84 C 451 a) CXA 1840 b) MSM 5299 c) 86 C 864 a) ET 4 000 b) MSM 5839 c) 86 C 964 a) M 50452 b) PCF 8576 c) AVGA1 a) MB 89321 b) SED 1520 c) CL-GD5410 a) MB 89322 b) SED 1521 c) HT 208 a) TVGA 8900C b) SED 1600 c) HT 209 a) V 6363 b) SED 1610 c) L 64845 a) VY 86 C 310 b) T 6A39 c) MVM 121A a) WD 90 C 10 b) T 6A40 c) NCR 77C22 a) WD 90 C 11 b) TMS 3491 c) OTI 067 a) WD 90 C 30 b) TMS 3492 c) PEGA a) WD 90 C 31 b) TMS 57202 c) PVGA a) WD 90 C 33 b) TMS 57206 c) SC 1 5064 b) 82 C 425 b) TMS 57207 c) WD 90 C 00 b) CL-GD6410 b) TMS 57210 d) 82 C 433 b) COP 472 b) TMS 57212 b) H 5050 b) TMS 57213 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*28	Error detection and correction circuit of N-MOS (including H-MOS) technology, capable of detecting and correcting single bit errors and detecting all double bit errors, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 8206 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*33	Bus interface circuit, whether or not with bus control functions, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 03H6300 89C105 L 64853A 53 C 700 94G0207 LIA 6396 53 C 710 AIC 6250 LIA 6732 53 C 720 AIC 7770 MB 86980 82335 CL PD6710 NCR 5380 82351 CL PD6720 NCR 5381 82352 CY7C960 NCR 53 C 80 82353 CY7C964 NCR 53 C 90 82365SL ES 688 PBI 82375EB ESP 216 PCF 8574 82378IB ESP 226 TACT 84544 82423TX FAS 216 TMS 38030 82433LX FAS 226 WD 33 C 92 82C100 FAS 236 WD 33 C 93 82C300 FE 3030 WD 33 C 95 82C596 GC 132 WD 33 C 96 82C611 GC 133 WD 76 C 10 82C836 HDL 33A112-00HQ Z 86017 89C100 HS 3282 or	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56 *(cont'd)*		— other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*35	Interface circuit or control circuit, for a local area network (LAN), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 8003 Am 79C90 DP 8025 MB 86950 80C03 Am 79C940 DP 83251 MB 86965A 82586 Am 79C950 DP 83255 SMC 83C790 82588 Am 79C960 DP 83261 T 7213 82590 Am 79C961 DP 83265 WD 80 C 24 82592 Am 79C965 DP 8390 WD 83 C 503 83C795 Am 79C970 DP 83902 WD 83 C 510 Am 7990 Am 79C987 DP 83905 WD 83 C 603 Am 79C830 COM 9026 DP 83932 WD 83 C 690 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56 ex 8542 11 83	*38 *38	Serial interface, capable of implementing the data stream encoding, decoding and associated control functions for a local area network (LAN), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 8002 82501 AM 7991 COM 91 C 32 8023 82 C 501 COM 9032 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*59	Arithmetic-logic unit (ALU) of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY2901 CY7C9115 CY7C9117 CY7C9101 CY7C9116 CY7C901 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*63	Adaptive differentiated pulse-code-modulation encoder/decoder of C-MOS technology, comprising a pulse-code-modulation encoder/decoder interface circuit, a transmit and receive control circuit, a microprocessor bus interface circuit and a parallel port, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VP 06565 VP 23070 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 63	*12	Register of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74FCT162374 74FCT16823BT 74FCT574 74FCT162823BT 74FCT16823CT Am 29C818A 74FCT162823CT 74FCT374 Am 29C821A 74FCT16374 74FCT534 Am 29C823A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63 ex 8542 11 69	*24 *04	Transmitter/receiver of BiMOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74ABT543 DS 3884 SN 74 BCT 2424 CY7B923 DS 3886 SN 74 BCT 2425 CY7B933 SN 74 BCT 2420 SN 75 LBC 976 CY7B955 SN 74 BCT 2423 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*12	Quadruple digital–to–analogue converter with a capacity of 12 bits, of BiMOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 664 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*14	Digital–to–analogue converter of C–MOS technology, with at least one of the following characteristics: a) with a capacity of 8 bits, capable of converting serial data input towards 36 output channels, b) with a capacity of 8 bits, capable of double buffering 8–bit words, c) with a capacity of 8 bits, with an output buffer amplifier, a serial interface circuit and at least 12 channels, d) single or triple converter, with at least one random–access memory (RAMDAC), having one or more colour palette registers, e) with a dynamic audio range of 90 dB or more, f) 8– or 10–bit video converter, with 3 channels for the separate conversion of colour signals, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) M 62352P d) ATT 20C493 d) Bt473 d) TR 9C1710 b) DAC 0830 d) ATT 20C497 d) Bt475 d) TVP 3020 b) DAC 0831 d) Bt445 d) MU 9C9760 d) TVP 3030 b) DAC 0832 d) Bt451 d) SC 11482 e) CS 4328 c) MB 88344B d) Bt458 d) SC 11483 e) CXD 2564 d) 357S0010 d) Bt459 d) SC 11484 e) TMS 57010 d) 357S0011 d) Bt460 d) SC 11485 f) CXD 1178 d) 357S0012 d) Bt461 d) SC 11487 f) CXD 2309 d) ATT 20C490 d) Bt462 d) SC 11489 d) ATT 20C491 d) Bt463 d) SC 15025 d) ATT 20C492 d) Bt467 d) SC 15026 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*15	Analogue–to–digital converter, with at least one of the following characteristics: a) 8–bit parallel converter of C–MOS technology, b) with a capacity of 16 or 20 bits of C–MOS technology, comprising a synchronisation circuit, 2 modulators, 2 digital filters, a 4–bit digital–to–analogue converter and an amplifier, c) 16–, 18– or 20–bit stereo audio converter of C–MOS technology, d) with a capacity of 16 bits, comprising a digital filter with a passband of 45,5 kHz at 3 dB, e) capable of driving a liquid crystal (LCD) or light emitting diode (LED) display with not more 4 digits, f) 8–bit video converter of C–MOS technology, comprising a synchronising clamp circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one the following combinations of figures and letters: a) IDT 75C48 c) CS 5339 e) ICL 7137 a) IDT 75C58 c) CS 5349 e) MAX 130 a) MP 7683 d) DSP 56ADC16 e) MAX 131 a) MP 7684 e) HI 7131 e) MAX 133 b) CS 5516 e) HI 7133 e) MAX 138 b) CS 5520 e) ICL 7106 e) MAX 139 c) CS 5326 e) ICL 7107 e) MAX 140 c) CS 5327 e) ICL 7116 e) MAX 136 c) CS 5328 e) ICL 7117 f) CXD 1176 c) CS 5329 e) ICL 7126 f) CXD 2300 c) CS 5336 e) ICL 7136 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*18	Data segmentation or reassembly circuit of C–MOS technology, providing fragmentation of 16 382 packets of 8– or 16–bit words into cells or providing reassembly of these cells in 16 382 packets of 8– or 16–bit words, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TXC 05501 TXC 05601 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*23	Subscriber line audio–processing circuit (SLAC) of C–MOS technology, comprising 2 digital signal processors, at least 1 analogue–to–digital converter and at least 1 digital–to–analogue converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 7901 Am 7905 Am 79C02 Am 79C03 Am 79C04 or — other identification markings relating to devices complying with the abovementioned description	7

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*29	Signal synthesiser of N–MOS (including H–MOS) technology with a frequency generator, a memory of 15 instrumental tones, a digital–to–analogue converter and a quartz oscillator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: YM 2413 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*31	Video processing circuit of C–MOS technology, having subpicture display (picture–in–picture) functions for colour television, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD 2033 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*36	Audio decoder of C–MOS technology, capable of decoding and decompressing audio signals at a rate per second not exceeding 15 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74 ACT 6350 TMS 320AV120 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*49	Clock generator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 32 D4661CL, 82 C 402, AV 9129, Bt 438, Bt 439, CXD 1035, CXD 1252, CXD 1255, CY7B991, CY7B992, DP 8531, DP 8532, DP 83241, ICD 2023, ICD 2027, ICD 2028, ICS 1394, ICS 2494, ICS 90C64, ICS 9161, LZ 93F31, LZ 93F33, LZ 93N61, MK 1418, MK 1442, MK 1448, MK 1450, MSM 5547, PCLK 1, PCLK 2, SC 11410, SC 11411, SC 11412, TCK 9002, WD 90 C 61 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*52	Circuit for the recording and reproduction of speech of C–MOS technology, working at a speed of 8 Kbits/sec or more, with at least one of the following caracteristics: a) comprising an amplifier and a 10–bit digital–to–analogue converter, b) comprising a memory interface circuit, an encoding/decoding circuit, a central processing unit (CPU) interface, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) T 6668 a) TC 8830 b) TC 88401 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 73	*15	Random-access memory of ECL technology (ECL-RAM) with a storage capacity not exceeding 64 Kbits, in the form of a monolithic integrated circuit, either contained in a housing or fixed on a plastic support, and bearing: — an identification marking consisting of or including one of the following combinations of figures: 10422 10474 10484 100474 100490 10470 10480 10490 100484 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 97	*15	Subscriber line interface circuit (SLIC) of dielectric isolation technology, with an internal programmed constant line current, comprising a resistor network and an operational amplifier, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HC 5502 HC 5504 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*11	Transmitter/receiver of bipolar technology, capable of converting data into serial or parallel format and serial data transfer at a rate not exceeding 200 megabytes per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 100SX1451 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*12	Transmitter/receiver of bipolar technology, capable of data transmission over a twisted-pair cable, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DP 83220 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*70	Prescaler of bipolar technology, having an input frequency not exceeding 2,8 GHz and a selectable 32/33, 64/65, 64/128 or 128/129 divide ratio, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 12022 MC 12034 MC 12053 SC 12022 MC 12032 MC 12052 MC 12089 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 10	*50	Wafer, not yet cut into chips, consisting of speech circuits of C-MOS technology, for use in the manufacture of goods of subheading 8542 19 95 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AS 2520 AS 2531 or — other identification markings relating to devices complying with the abovementioned description (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 20	*60	Amplifier with an input current not exceeding 80 nA, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: INA 101 OPA 111 OPA 121 OPA 2111 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 20	*70	Amplifier with a programmable gain factor, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 20 50 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 3606G or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 30	*01	Intermediate frequency (IF) or FM amplifier of bipolar technology, comprising a mixer, a receive signal strength indicator (RSSI), a detector and an oscillator in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1343 CXA 1744R or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*08	Amplifier of gallium arsenide (GaAs) semiconductor material, having a nominal gain of 15,4 dB or more but not exceeding 30 dB and a frequency range of not more than 8 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G071 16G072 16G074 MGA 86576 MGF 7131 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*09	Audio amplifier of bipolar technology, with a typical gain of 47 dB in a frequency range of 20 Hz to 20 kHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TA 201S or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*30	Dual or quadruple amplifier operating with a supply current per amplifier not exceeding 1,4 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 014B LT 1079 LT 1179 MC 14574 OP 292 LT 1078 LT 1178 MC 14573 MC 14575 OP 492 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 50	*20	Voltage regulator with an input voltage range of 3 V or more but not exceeding 60 V and a quiescent current of 6 or 7 mA, comprising an internal 1,25 A, 2,5 A, 4 A or 5 A switch circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1070 LT 1071 LT 1170 LT 1171 LT 1172 LT 1271 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*55	Voltage regulator, with an input voltage of –15 V or more but not exceeding 60 V, an output voltage of 1 V or more but not exceeding 9,15 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BA 3910–V2 CS 8141 LT 1149 CS 8140 LT 1142 S 812 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 61	*30	Smartpower circuit, capable of controlling battery voltage charge, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MPC 1825VM or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*30	3–phase DC motor control circuit of bipolar technology, comprising an oscillator, power and phase changeover circuits and a ring counter, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AN 8225 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*40	Circuit for driving linear motors or motors with rotating arms, of C–MOS technology, comprising a drive current switching circuit and a power fault detection circuit, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 32H6810 50G2996 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*65	Video control circuit of bipolar technology, capable of switching and clamping video signals, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1860 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 69	*22	Control circuit of bipolar technology, providing volume control, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BA 3574 CXA 1646 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*53	Control circuit, capable of driving power field-effect transistors (FETs), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 71009SB LTC 1155 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*40	Subscriber line interface circuit (SLIC) of bipolar technology, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 79M535 Am 79M574 Am 79M576 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 19 95	*11	Audio circuit of C-MOS technology, with a dynamic range of 80 dB or more, comprising 2 digital-to-analogue converters and 2 analogue-to-digital converters, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 1848 CS 4231 CS 4248 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*14	Voice signal processing circuit of C-MOS technology, comprising an encoding circuit, a decoding circuit, a compression circuit, a decompression circuit and a modulator/demodulator (Modem) for full data-transfer at a speed of 1 200 or 2 400 bits/s, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AK 2353 TC 35492 TC 35493 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*22	Frequency synthesiser, operating with an input frequency not exceeding 2 GHz and a DC supply voltage not exceeding 10 V, comprising a phase-locked loop (PLL) circuit and a programmable 14-bit reference counter, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LMX 2320 MC 145158 MC 145162 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 95	*46	Passive decoder of BiMOS technology, comprising a fixed matrix, a 7-kHz filter, a noise-reducing circuit, a digital delay circuit and a memory-control circuit, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LV 1 000 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*48	Matrix decoder, comprising an adaptive matrix circuit, a noise generator and a automatic-balance control circuit, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LA 2785 M 69032P SSM 2125 SSM 2126 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*68	Video processing circuit of bipolar technology, providing discrimination of synchronisation signals, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1616 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*76	Video processing circuit of bipolar technology, for colour and synchronisation signals, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1213BS CXA 1587 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*78	Signal measurement circuit for current and position sensors, of C-MOS technology, comprising 3 analogue-to-digital converters, a digital-to-analogue converter, multiplexers and sample and hold control circuits, in the form of a monolithic integrated mixed analogue/digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VECANA 01 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 19 99	*04	Speech-transfer circuit of bipolar technology, comprising a transmitted signal attenuator, a received signal attenuator, an attenuator controller, a mute controller, 3 amplifiers, a dial tone detector and 2 noise generators, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 34118 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*12	FM–band receiver of bipolar technology, providing FM–signal demodulation, comprising at least a mixer, an intermediate frequency (IF) amplifier, a limiter amplifier and a data slicer, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 13156 MC 13158 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*20	Attenuator circuit of gallium arsenide (GaAs) semiconductor material, providing a voltage variable attenuation range not exceeding 40 dB at a frequency of 0,9 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AT 108 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*45	Video processing circuit of bipolar technology, for colour or luminance signals, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1207 CXA 1208 CXA 1779P or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*52	Voltage comparator, operating within a common voltage range of –12 V or more but not exceeding +12 V and a differential voltage range of –24 V or more but not exceeding +24 V and a response time not exceeding 6 ns, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: EL 2019 LT 1016 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*83	Phase–locked loop (PLL) circuit of bipolar technology, comprising an oscillator and a frequency and/or phase detector, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M52319SP SN 28967 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*40	Amplifier with an input power of 1 mW and an output power not exceeding 3,5 W at a frequency range of 890 MHz or more but not exceeding 915 MHz or of 1 710 MHz or more but not exceeding 1 785 MHz, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: FA 01314 XHW 903 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8543 80 95	*23	Amplifier of bipolar technology, operating within a frequency range of 800 MHz to 950 MHz, with at least one of the following characteristics: a) an output power of 1,41 W at an input power of 5 mW, b) an output power of 2 W at an input power of 1 mW, c) an output power of 3,2 W at an input power of 2 mW, d) an output power of 3,5 W at an input power of 100 mW, e) an output power of 6 W at an input power of 100 mW, f) an output power of 14 W at an input power of 1 or 100 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) MHW 9002 c) PHW 902 e) SHW 5115 f) MHW 914 b) MHW 803 d) MHW 953 e) XHW 5115 f) MHW 915 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*41	Opto–electronic circuit comprising one or more light–emitting diodes (LEDs) and one photodiode with amplifier circuit and an integrated logic gate arrays circuit or one or more light–emitting diodes (LEDs) and at least 2 photodiodes with amplifier circuit, contained in a plastic housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HC PL 2 400 HC PL 2730 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*51	Temperature compensating frequency oscillator with a nominal frequency of 12,8 MHz and operating at a supply voltage of 3 V (±0,3 V), comprising a printed circuit on which are mounted at least a piezo–electric crystal and an adjustable capacitor, contained in a housing with not more than 5 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: TX 02603 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 90 90	*10	Dual field–effect transistor (FET) with at least one of following characteristics: a) of the P–channel type, having a drain–to–source breakdown–voltage of –20 V, operating with a drain–current not exceeding 9,2 A and with a dissipation rate not exceeding 2 W, b) of the N–channel type, having a drain–to–source breakdown–voltage of 20 V or more, operating with a drain–current not exceeding 3,5 A and with a dissipation rate not exceeding 2 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: a) 9947 a) MMDF2C02E b) 9956 b) MMDF1N50E a) 9953 a) MMDF2P02HD b) 9959 b) MMDF2C02E or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8548 00 00	*93	Memory module, comprising a printed circuit on which are mounted decoupling capacitors and at least 4 static random-access memories (S-RAMs) with a total storage capacity of 4 Mbits or more, contained in a housing the exterior dimensions of which do not exceed 15 x 109 mm, with not more than 69 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HB 6612832IB-7 MCM 32256 MCM 32A256 MCM 32128 MCM 32A128 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8548 00 00	*97	Optical unit consisting of a laser diode, a photodiode and a lens, operating at a typical wavelength of 1 310 or 1 550 nm, contained in a housing	0
ex 9001 90 90	*20	Rear projection screen, comprising a Fresnel lens of plastic and a polarizing sheet of plastic, for use in the manufacture of products falling within subheading No 8528 10 (a)	0
ex 9001 90 90	*30	Lens of plastic, unmounted, having a focal length of 3,86 mm (±0,1 mm) and with a diameter not exceeding 8 mm, for use in the manufacture of compact disc players (a)	0
9013 80 30		Liquid crystal devices, other than active matrix liquid crystal devices	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.

TABLE I

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8471 92 80	*10	Input unit (so-called 'touchpad', the exterior dimensions of which do not exceed 50 x 62 mm, capable of matrix scanning and detection, consisting of 2 layers of measurement electrodes, a printed circuit, a capacitive matrix, 2 integrated circuits, discrete components and a connector, for use in the manufacture of products falling within heading 8471 (a)	0
ex 8471 93 51	*40	Drive-unit for rewritable optical phase change discs of the 5,25 inch type, for use in the manufacture of products falling within heading 8471 (a)	0
ex 8471 93 51	*50	Drive-unit for magneto-optical discs, for use in the manufacture of products falling within heading 8471 (a)	0
ex 8471 93 59	*10	Floppy-disc storage units	0
ex 8471 93 59	*20	Disc storage unit, capable of parallel data-transfer via 1, 4, 5 or 6 channels at, respectively, a rate per second of 3,014 megabytes, 12,05 megabytes, 15,07 megabytes or 18,08 megabytes, comprising 8 rigid magnetic discs of the 8 inch type with a total storage capacity, unformatted, not exceeding 1 000,2 megabytes and incorporating a storage-module-drive (SMD) interface, for use in the manufacture of cardiodiagnostic apparatus (a)	0
ex 8471 93 59	*30	Disc storage unit of the 8 inch type, capable of parallel data-transfer via 1 channel at a rate per second of 3,041 megabytes, comprising a storage-module-drive (SMD) interface and 11 rigid magnetic discs with a total storage capacity, unformatted, not exceeding 2,5 gigabytes, for use in the manufacture of products falling within subheading 8471 91 80 (a)	0
ex 8471 93 59	*40	Disc storage unit of the 5,25 inch type, capable of external data-transfer at a rate per second of 7,5 megabytes, having dual channels for simultaneaously reading and writing with 2 magnetic heads, comprising a dual port interface circuit and 11 rigid magnetic discs with a total storage capacity, unformatted, of 1 986 megabytes, for use in the manufacture of products falling within subheading 9022 11 00 (a)	0
ex 8471 93 60	*10	Twin reel drive-unit of the 8 mm cartridge type, for use in the manufacture of magnetic tape storage units (a)	0
ex 8471 93 60	*12	Drive-unit, incorporating a recording drum, for use in the manufacture of digital audio tape storage units (a)	0
ex 8471 93 60	*20	Magnetic tape storage unit for single-reel cartridges	0
ex 8471 99 80	*10	Optical reader for reading alphanumeric dot-matrix printing characters and converting them into electrical signals, comprising a read head containing an optical detector, an amplifier, a focusing lens and two lamps, linked by one or two flat cables to a central module the dimensions of which do not exceed 200 x 220 mm, comprising a printed circuit board on which are mounted a microprocessor, an image recognition circuit and an analogue-to-digital converter	0
ex 8473 30 10	*15	Processor, consisting of: — 15 monolithic integrated circuits, comprising an arithmetic-logic unit (ALU) of 32 bits, a halfword arithmetic-logic unit (ALU), a halfword multiplier, a floating point unit, a fixed point unit, a storage control unit, a storage interface circuit and 10 static random-access memories (S-RAMs) with a total storage capacity of 5 760 Kbits,	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8473 30 10 *(cont'd)*		— decoupling capacitors and cooling plates, the whole mounted on a multilayer ceramic substrate the exterior dimensions of which do not exceed 65 x 65 mm, with not more than 624 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G7559 16G7620 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*20	Processor of ECL technology, consisting of not more than 336 monolithic integrated circuits, each comprising not more than 15 000 programmable logic arrays, mounted on one or both sides of a multiple printed circuit, contained in a housing attached to a cooling plate or enclosed between two cooling plates, the overall exterior dimensions of which do not exceed 148 x 560 x 594 mm and bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 001B-3035-H002 52-203619 52-203621 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*25	Processor, consisting of: — 12 monolithic integrated circuits, comprising 2 central processing units (CPUs) with an integer/floating point unit, 2 cache controllers, memory management and tag units (CMTUs) and 8 static random-access memories (S-RAMs) with a total storage capacity of 4 Mbits, — decoupling capacitors and cooling plates, the whole mounted on a multilayer ceramic substrate the exterior dimensions of which do not exceed 84 x 147 mm, with not more than 100 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: RT 6626K RT 6236K or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*30	Component forming the arithmetic/logic element of a central processing unit (CPU), comprising not more than 9 printed circuit boards, the dimensions of which do not exceed 290 x 310 mm, on each of which are mounted not more than 121 ECL gate arrays or ECL random access memories (ECL-RAMs) and combinations thereof, contained in a framework the dimensions of which do not exceed 501 x 596 x 611 mm which serves as a housing and interconnector for the printed circuit boards, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CO1B 2675 E 500 CO1B 2675 H 501 CO1B 2675 H 503 CO1B 2675 H 500 CO1B 2675 H 502 CO1B 2675 H 504 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 10	*35	Processing system, consisting of: — not more than 121 monolithic integrated circuits not contained in a housing (chips), — a ceramic substrate, the whole enclosed between a metallic baseplate and a metallic plate incorporating not more than 121 cooling pistons filled with liquid	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8473 30 10	*45	Memory module, consisting of at least 2 read only memories, non–programmable (ROMs)	0
ex 8473 30 10	*50	Assembly for disc storage units of Winchester technology, comprising a 2– or 4–channel read/write monolithic integrated circuit for magnetic head signals mounted with discrete components on a flexible printed circuit	0
ex 8473 30 10	*55	Flash electrically erasable, programmable, read only memory (Flash–E²PROM) with a storage capacity of 32 Mbits, consisting of 2 monolithic integrated circuits contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 28 F 032SA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 90	*15	Metal ball coated with plastic or rubber material, for use in the manufacture of a pointing device (so–called 'mouse') (a)	0
ex 8473 30 90	*20	Ferrite magnetic heads of Winchester technology, for hard disc file–peripherals as well as carrying arms equipped with such magnetic heads, capable of recording to a density of 10 tracks or more per mm	0
ex 8473 30 90	*30	Read/write assembly for hard disc storage units, comprising ferrite magnetic heads of Winchester technology, mounted on a carrier arm, connected via a flexible printed circuit to a metal bracket upon which are mounted: — a flexible printed circuit upon which is mounted an amplifier for read/write signals, in the form of an integrated circuit, — a printed circuit socket	0
ex 8473 30 90	*35	Mechanical encoder, consisting of a disc, contact–brushes, a bearing, a spacer and a rotational shaft, mounted in a casing with 3 connections, and having a withstanding voltage of 50 V AC for at least 1 minute	0
ex 8473 30 90	*45	Rotor arm without electronic components, for read/write assemblies of hard disc storage units	0
ex 8473 30 90	*50	Magnetic bubble memories with a storage capacity of not more than 4 Mbits, contained in a housing the exterior dimensions of which do not exceed 43 x 44 mm, with not more than 56 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 7110 FBM 54 DB BDL 0133 BDN 0151 MBM 2011 7114–1 FBM 64 DA BDL 0134 BDN 515 MBM 2256 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8473 30 90	*55	Data storage assembly (Head/Disc/Assembly) for disc storage units, with a data transfer rate per second of 3,9 or 4,2 megabytes, comprising read/write heads and 9 or 11 rigid magnetic discs with an external diameter not exceeding 24,2 cm (9,5 inch) with a total storage capacity, formatted, of 2 838 or 8 514 megabytes, the whole incorporated in a single hermetically sealed housing	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8473 30 90	*60	Data storage assembly (Head/Disc/Assembly) for disc storage units of the 9 inch type, with a data transfer rate per second of 2,77 megabytes, comprising read/write heads and 7 rigid magnetic discs with a total storage capacity, formatted, of 1 216 or 1 506 megabytes, operating with a supply voltage of 120 V and of 220 V or more but not exceeding 240 V, the whole incorporated in a single hermetically sealed housing	0
ex 8473 30 90	*65	Drive-unit, not incorporating a recording drum, for use in the manufacture of digital audio tape storage units (a)	0
ex 8473 30 90	*70	Data storage assembly (Head/Disc/Assembly) for disc storage units of the 10,8 inch type, with a data transfer rate per second of 3,9 megabytes, comprising 16 read/write heads and 9 rigid magnetic discs with a total storage capacity, formatted, of 17 gigabytes, the whole incorporated in a single hermetically sealed housing	0
ex 8473 30 90	*75	Data storage assembly (Head/Disc/Assembly) for disc storage units of the 3,5 inch type, with a data transfer rate per second of 4,7 or more but not exceeding 6,9 megabytes, comprising read/write heads and 15 rigid magnetic discs with a total storage capacity, formatted, of 4 361 megabytes, the whole incorporated in a single hermetically sealed housing	0
ex 8473 40 90	*10	Thermal printer heads of thick- or thin-film technology, consisting of a printed circuit with at least one tantalum capacitor and an electrolytic capacitor contained in a metal support with connector, printer element and heat sink, supplied with the appropriate support and transport roll	0
ex 8473 40 90	*85	Thermal printer head of thin-film technology, the exterior dimensions of which do not exceed 18 × 90 × 275 mm, consisting of: — a printed circuit on a ceramic substrate fitted with monolithic integrated circuits and 2 880 heater elements, — a printed circuit fitted with monolithic integrated circuits, capacitors, resistors and connectors, — a thermistor and — 1 or 2 cooling plates	0
ex 8483 10 90	*10	Integrally forged and roughly shaped generator and turbine shafts of a weight exceeding 215 tonnes	0
ex 8501 10 99	*52	DC motor, brushless, with a typical running torque not exceeding 0,20 Nm, with a coupling flange of a diameter of 76 mm (±1 mm), with an outside rotor of a diameter not exceeding 55 mm (±1 mm), a four-phase winding, a rated speed of 3 600 (±1 %) rpm, a supply voltage of 12 V (±10 %) and fitted with wires and connectors	0
ex 8501 10 99	*53	DC motor, brushless, with a typical running torque of 0,00025 Nm (±0,00005 Nm), with a coupling flange of a diameter of 6,8 mm (±0,1 mm) and a chuck of a diameter of 12 mm (±1 mm), with an external rotor, a three-phase winding, a rated speed of 4 800 (±1 %) rpm and a supply voltage of 4 V (±25 %)	0
ex 8501 10 99	*54	DC motor, brushless, with an external diameter not exceeding 25,4 mm, a rated speed of 2 260 (±15 %) or 5 420 (±15 %) rpm, a supply voltage of 1,5 or 3 V	0
ex 8501 10 99	*58	DC motor, with brushes, with a diameter of 6 mm (±0,2 mm) or 7,1 mm (±0,2 mm), a shaft of a diameter of 1 mm (±0,02 mm), a rated speed of 6 000 (±24 %) rpm and a supply voltage of 1,25 V (±36 %)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8501 10 99	*59	DC stepping motor, with an angle of step of 1,8° (±0,09°), a holding torque of 0,156 Nm or more, a coupling flange the exterior dimensions of which do not exceed 43 x 43 mm, a chuck of a diameter of 4 mm (±0,1 mm), a two-phase winding and an output not exceeding 5 W	0
ex 8501 10 99	*69	DC motor, brushless, with a typical running torque not exceeding 0,002 Nm, with a coupling flange of a diameter of 27,5 mm (±0,5 mm), a coupling flange of a diameter of 15 mm (±0,5 mm) and a chuck of a diameter of 25 mm (±0,5 mm), with an internal rotor, a three-phase winding, a rated speed of 3 409 (±0,1 %) rpm and a supply voltage of 12 V (±5 %)	0
ex 8501 10 99	*70	DC motor, brushless, with a typical running torque not exceeding 0,001 Nm, with a coupling flange of a diameter of 57 mm (±0,5 mm), a coupling flange of a diameter of 43,5 mm (±0,5 mm) and a chuck of a diameter of 25 mm (±0,5 mm), with an internal rotor, a three-phase winding, a rated speed of 3 575 (±0,1 %) rpm and a supply voltage of 12 V (±5 %)	0
ex 8501 10 99	*71	DC bipolar motor, brushless, with a typical running torque not exceeding 0,0223 Nm, with a coupling flange of a diameter of 17,8 mm (±0,5 mm) and a chuck of a diameter of 25 mm (±0,5 mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 (±0,1 %) rpm and a supply voltage of 12 V (±10 %)	0
ex 8501 10 99	*72	DC bipolar motor, brushless, with a typical running torque not exceeding 0,00825 Nm, with a coupling flange of a diameter of 27,4 mm (±0,5 mm) and a chuck of a diameter of 25 mm (±0,5 mm), with an internal rotor, a three-phase winding, a rated speed of 4 498 (±0,1 %) rpm and a supply voltage of 12 V (±10 %)	0
ex 8501 10 99	*74	DC motor, brushless, with a typical running torque not exceeding 0,0071 Nm, with a coupling flange of a diameter of 12,7 mm (±0,5 mm) and a spindle of a diameter of 39,7 mm (±1 mm), with a rated speed of 3 600 (±0,1 %) rpm and a supply voltage of 11 V (±10 %)	0
ex 8501 10 99	*76	DC motor, coreless, with a diameter of 18 mm (±0,1 mm), a shaft with a diameter of 0,8 mm (±0,02 mm), a rated speed of 5 000 (±20 %) rpm and a supply voltage of 1,25 V (±36 %)	0
ex 8501 10 99	*77	DC motor, with brushes, with a typical running torque of 0,004 Nm (±0,001 Nm), with a coupling flange of a diameter of 32 mm (±0,5 mm) and a chuck of a diameter of 2 mm (±0,004 mm), with an internal rotor, a three-phase winding, a rated speed of 2 800 (±10 %) rpm and a supply voltage of 12 V (±15 %)	0
ex 8502 40 90 ex 8504 31 90	*10 *20	Transformer, with a ferrite core, having coils with 2 or 6 windings and a diameter of 0,1 mm, connected to a flexible printed circuit	0
ex 8504 40 98	*10	Direct current to direct current converter, with an input voltage range of 100 V or more but not exceeding 390 V, contained in a housing	0
ex 8504 50 90	*10	Inductor with a variable inductance not exceeding 62 mH	0
ex 8504 50 90	*20	Multilayer monolithic inductors, contained in a housing of the SMD (Surface mounted device) type the exterior dimensions of which do not exceed 1,8 x 3,4 mm, for use in the manufacture of pagers (a)	0
ex 8505 90 10	*91	Solenoid with a plunger, operating at a nominal supply voltage of 24 V at a nominal DC of 0,08 A, for use in the manufacture of products falling within heading No 8517 (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8506 19 19	*10	Lithium iodine single cell battery the dimensions of which do not exceed 9 x 23 x 45 mm and a voltage not exceeding 2,8 V	0
ex 8506 19 19	*20	Unit consisting of not more than 2 lithium batteries embedded in a socket for integrated circuits (battery-buffered socket), with not more than 32 connections and incorporating a control circuit	0
ex 8507 30 91	*10	Rectangular nickel-cadmium accumulator, with a length of 67 mm (±0,1 mm), a width of 16,8 mm (±0,05 mm) and a thickness of 10,5 mm (±0,05 mm), having a nominal capacity not exceeding 1 200 mAh, for use in the manufacture of rechargeable batteries (a)	0
ex 8507 90 98	*10	Rectangular nickel-hydrid accumulator, with a length of 48,4 mm (±1 mm), a width of 14,5 mm (±1 mm) and a thickness of 7,5 mm (±1 mm), having a capacity of 500 mAh or more and a nominal voltage of 1,2 V, for use in the manufacture of rechargeable batteries (a)	0
ex 8517 82 90	*10	Transmitter, capable of converting electrical signals into light pulses, operating at a nominal wavelength of 850 nm, comprising a light-emitting diode (LED), a current switch, an input buffer and a distortion/compensation circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DM-231-TA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 82 90	*20	Receive unit, capable of converting light pulses into electrical signals, operating at a nominal wavelength of 850 nm, comprising a photodiode, 2 decision circuits, an amplifier and an integrator, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DM-231-RA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 11	*01	Modulator/demodulator of C-MOS technology (C-MOS-Modem), for full duplex data-transfer at a rate of 28 800 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate of 14 400 bits per second, only consisting of 2 or more monolithic integrated circuits, at least one of which for digital signal processing (DSP) and an other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: RC 192DP RC 192DPL RC 240DP RC 240DPL RC 288DP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 11	*02	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of data or image telegraphy (facsimile) at a rate not exceeding 2 400 bits per second, only consisting of 2 monolithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: RC 24BKJ or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8517 90 11	*03	Modulator/demodulator of C–MOS technology (C–MOS–Modem), only for full duplex data–transfer at a rate not exceeding 2 400 bits per second, only consisting of 2 monolithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: RC 2324DPL or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 11	*04	Modulator/demodulator of C–MOS technology (C–MOS–Modem), only for half duplex transfer of image telegraphy (facsimile) at a rate not exceeding 9 600 bits per second, only consisting of 2 monolithic integrated circuits, one of which for digital signal processing (DSP) and the other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: R 96DFX R 96EFX R 96MFX or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 11	*05	Modulator/demodulator of C–MOS technology (C–MOS–Modem), for full duplex data–transfer at a rate not exceeding 9 600 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate not exceeding 9 600 bits per second, only consisting of 2 or 3 monolithic integrated circuits, 1 or 2 of which for digital signal processing (DSP) and an other for analogue functions, mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: RC 9323 RC 9624 RC 96DPL RC 96V24 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 81	*10	Assembly for telephonic apparatus comprising a microphone, a protecting circuit and a four–way connecting socket, mounted on a printed circuit the dimensions of which do not exceed 22 × 40 mm	0
ex 8517 90 81	*20	16 × 16– or 32 × 32–bit differential crosspoint switch of gallium arsenide (GaAs) semiconductor material, capable of switching at a data rate per second of at least 800 Mbits, in the form of a monolithic integrated circuit contained in a housing combined with decoupling capacitors, the whole mounted on a substrate the exterior dimensions of which do not exceed 35 × 35 mm, with not more than 196 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TQ 8016 TQ 8032 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8517 90 81	*30	Assembly consisting of a laser diode operating at a nominal wavelength of 780 nm, a photodiode and a lens, contained in a housing with a diameter of not more than 9 mm and a height of not more than 20 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combination of letters or of figures and letters: FU-011SLD-N2 LM-7115 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8517 90 92	*10	Electronic assembly for a thermal printer head, consisting only of conductor elements, monolithic integrated circuits and at least 8 640 heater elements, the whole mounted on a ceramic substrate the exterior dimensions of which do not exceed 3 x 61 x 271 mm	0
ex 8517 90 98	*05	Assembly consisting of a laser diode operating at a nominal wavelength of 980 nm, a photodiode, a thermistor and a cooling plate, contained in a housing with an optical fibre cable connection and bearing: — an identification marking consisting of or including the following combination of figures and letters: QLM9S470 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8518 30 90	*10	Headphone and earphone for hearing aids, contained in a housing the exterior dimensions of which, excluding connecting points, do not exceed 5 x 6 x 8 mm	0
ex 8518 90 00	*91	Integrally cold-upsetted steel coreplate, in the form of a disc on one side provided with a cylinder, for use in the manufacture of loudspeakers (a)	0
ex 8522 90 91	*91	Optical unit consisting of a laser diode with one photodiode, emitting light of a nominal wavelength of 780 nm, contained in a housing with a diameter of not more than 10 mm and a height of not more than 9 mm, with not more than 10 connections and bearing: — an identification marking consisting of or including one of the following combination of letters or of figures and letters: LDGU LT 022 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8522 90 98	*33	Assembly consisting of a driver circuit, a tacho-sensor and a brushless DC motor, with a typical running torque of 0,0044 Nm (±0,001 Nm), a shaft of a diameter of 3,523 mm (±0,002 mm), an external rotor of a diameter of 69 mm (±0,3 mm), a three-phase winding, a rated speed of 2 600 (±16 %) rpm and a supply voltage of 14 V (±10 %)	0
ex 8522 90 98	*34	Cassette-deck sub-assembly for sound recording and reproducing apparatus, for use in the manufacture of telephone answering machines (a)	0
ex 8522 90 98	*35	Sound reproducing assembly, comprising a tape deck mechanism of the cassette type, comprising a DC motor, for use in the manufacture of products falling within heading 8519 (a)	0
ex 8522 90 98	*36	Roll for magnetic tape guiding and winding, for use in the manufacture of products falling within heading No 8521 or 8522 (a)	0
ex 8522 90 98	*37	Magnetic head for erasing video tapes, for use in the manufacture of products falling within heading No 8521 or 8522 (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8523 20 19	*40	Rigid magnetic discs, prelubricated, oxide type, with a coercivity of 300 Oe or more, not mounted in a cartridge	0
ex 8525 20 90	*10	Radio frequency (RF) transmitter/receiver for digital cordless telephones, for frequencies of 1,8 GHz or more but not exceeding 1,9 MHz and having a nominal supply voltage of 3,6 V, contained in a housing mounted on a printed circuit	0
ex 8528 20 20	*10	Video monitor comprising: — a flat screen monochrome cathode–ray tube with a diagonal measurement of the screen not exceeding 110 mm and equipped with a deflector yoke, and — a printed circuit on which are mounted a deflection unit, a video–amplifier and a transformer, the whole mounted on a chassis, for the manufacture of video entry–phones, video telephones or surveillance apparatus (a)	0
ex 8529 10 70	*10	Ceramic filter package comprising 2 ceramic filters and 1 ceramic resonator for a frequency of 10,7 MHz (±30 kHz), contained in a housing	0
ex 8529 10 70	*20	Ceramic filters for frequencies of 4,5 MHz or more but not exceeding 6,6 MHz contained in a housing	0
ex 8529 10 70	*30	Ceramic filter for a frequency of 450 kHz, with a bandwidth not exceeding 18 kHz at 10 dB, contained in a housing	0
ex 8529 10 70	*40	Radio frequency (RF) signal isolator for frequencies of 940 MHz or more but not exceeding 1 453 GHz, having an insertion loss not exceeding 0,7 dB, contained in a housing	0
ex 8529 90 81	*31	Demagnetisation coil with not more than 96 windings, with cables and connectors	0
ex 8529 90 81	*32	Assembly comprising a lens unit, having a focal length of 3,6 mm, an interline charge–coupled image sensor having 291 000 photosensitive cells, and integrated circuits, the whole mounted on a printed circuit	0
ex 8529 90 81	*34	Assembly consisting of a lens unit, having an adjustable focal length of 5 mm or more but not exceeding 69 mm and comprising a zoom encoder, a stepping motor unit, a zoom motor unit, an iris motor unit and a photo interrupter	0
ex 8529 90 81	*35	Video recording and reproducing assembly, comprising a tape deck mechanism of the cassette type, comprising a DC motor, for use in the manufacture of products falling within heading 8525 (a)	0
ex 8529 90 81	*36	Assembly consisting of a monochrome cathode–ray tube with a diagonal measurement of the screen of 165 mm or more but not exceeding 230 mm and a concave focus lens mounted on a liquid–filled cooling armature, for use in the manufacture of television projection equipment (a)	0
ex 8529 90 81 ex 8529 90 89	*37 *31	Filter, consisting of 2 piezo–electric crystals each with a frequency of 21 MHz or more but not exceeding 30 MHz and seperately mounted on a bracket, with not more than 7 connections	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8531 20 30	*10	Dot matrix display consisting of a line of 8 characters, each character composed of 35 light-emitting diodes (LEDs), comprising electronic components for interface and drive functions, contained in a housing the exterior dimensions of which do not exceed 20 x 43 mm, with not more than 28 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HDSP 2107 HDSP 2112 PDSP 2110 PDSP 2112 HDSP 2111 HDSP 2113 PDSP 2111 PDSP 2113 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8531 20 30	*20	Digital displays, consisting of a printed circuit board of a size not exceeding 35 x 90 mm with a single line of characters, not less than 3 in number, comprising light-emitting diodes (LEDs) made from gallium-based semiconductor materials mounted thereon. Each character is composed of up to 8 segments with or without a decimal point and the line of characters has a protective cover of plastic	0
ex 8531 80 90	*10	Direct current plasma display	0
ex 8531 80 90	*20	Transducer, capable of producing a sound level of 85 dB at a frequency of 2 700 or 3 200 Hz	0
ex 8531 80 90	*30	Vacuum fluorescent display, consisting of a memory refresh circuit, a character generator, a DC/DC converter and electronic components providing drive and/or control functions	0
ex 8531 90 10	*91	Backlight unit, comprising a lampholder with a cathode tube, a reflection sheet and a diffuse substrate, the exterior dimensions of which do not exceed 7 x 250 x 300 mm, for use in the manufacture of liquid crystal displays (LCD) (a)	0
ex 8532 22 00	*95	Aluminium electrolytic capacitors, with a fixed nominal capacity not exceeding 470 µF and an operating voltage not exceeding 50 V, operating within a temperature range of –40 °C to +85 °C, having a diameter not exceeding 8 mm and a thickness not exceeding 5 mm	0
ex 8532 22 00	*96	Aluminium eletrolytic capacitors, with a fixed nominal capacity of 2,2 µF and an operating voltage of 385 V, operating within a temperature range of –40 °C to +85 °C	0
ex 8532 22 00	*97	Aluminium eletrolytic capacitor, with a fixed nominal capacity not exceeding 3,3 F and an nominal operating voltage of 2,5 or 5,5 V, operating within a temperature range of –25 °C to +85 °C	0
ex 8532 23 00	*91	One layer ceramic dielectric capacitor, with a fixed nominal capacity of 1 pF or more but not exceeding 1 µF and an nominal operating voltage not exceeding 50 V, operating within a temperature range of –25 °C to +85 °C	0
ex 8533 10 00	*92	Fixed carbon composition resistor, with an operating voltage not exceeding 350 V and a dissipation rate not exceeding 0,5 W	0
ex 8534 00 11 ex 8534 00 19	*91 *91	Single-face printed circuit the dimensions of which do not exceed 30 x 30 mm, for the manufacture of products falling within Chapter 91 (a)	0
ex 8534 00 11	*92	Multiple printed circuit, consisting of 24 layers, including 5 layers with buried vias of bismaleimide triazine, the exterior dimensions of which do not exceed 64 x 65 cm	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8534 00 19	*95	Printed circuit, consisting of conductor elements fixed on a flexible plastic film, with a trace width of 0,095 mm or more but not exceeding 3,5 mm and a trace pitch of 0,095 mm or more but not exceeding 0,305 mm, for use in the manufacture of electronic calculating machines (a)	0
ex 8534 00 19	*96	Printed circuit on an aluminium oxide support, only with gold plated conductor elements of thick film technology, for use in the manufacture of products falling within subheading 8542 20 50 (a)	0
ex 8534 00 90	*93	Printed circuit on one or both sides of a ceramic substrate, consisting of conductor elements, contacts and resistors, incorporating connections isolated in vitrified layers, the dimensions of which do not exceed 45 x 45 mm, with not more than 550 connections	0
ex 8536 41 10 ex 8536 41 90 ex 8536 49 00	*91 *91 *91	Thermal relays contained in a hermetically sealed glass cartridge not exceeding 35 mm in length excluding wires, with a maximum leakage rate of 10^{-6} cm^3 He/sec at one bar in the temperature range 0 to 160 °C, to be incorporated into compressors for refrigerating equipment (a)	0
ex 8536 50 90	*93	Switch unit for coaxial cable, comprising 3 electromagnetic switches, with a switching time not exceeding 50 ms and an actuating current not exceeding 500 mA at a voltage of 12 V	0
ex 8536 90 85	*91	Elastomeric connectors, consisting of conductor elements coated with gold and fixed on a substrate of rubber	0
ex 8538 90 90	*91	Part of an electrothermal fuse, consisting of a tin coated copper wire attached to a cylindrical casing, the exterior dimensions of which do not exceed 5 x 48 mm	0
ex 8540 11 11	*91	Colour cathode-ray tube with a slot mask, equipped with electron guns placed side by side (in-line technology) and with a diagonal measurement of the screen of 12 cm or more but not exceeding 26 cm	0
ex 8540 12 00	*82	Monochrome cathode-ray tube with a diagonal measurement of the screen of 250 mm or more but not exceeding 320 mm and an anode voltage of 18 kV or more but not exceeding 22 kV	0
ex 8540 20 90	*91	Photomultiplier consisting of a photocathode tube with 9 dynodes, for light of a wavelength of 160 nm or more but not exceeding 930 nm, of a diameter not exceeding 14 mm and a height not exceeding 94 mm	0
ex 8540 30 10	*34	Colour cathode-ray tube with a dot mask, equipped with 3 electron guns placed side by side (in-line technology) or 1 gun with 3 rays, with a diagonal measurement of the screen of more than 72 cm and a distance of less than 0,5 mm between dots of the same colour	0
ex 8540 30 10	*35	Colour cathode-ray tube with a dot mask, equipped with 3 electron guns placed side by side (in-line technology) or 1 gun with 3 rays, having a diagonal measurement of the screen not exceeding 72 cm	0
ex 8540 30 10	*36	Colour cathode-ray tube with a slit mask, having a distance between stripes of the same colour of less than 0,35 mm and a diagonal measurement of the screen not exceeding 53 cm	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8540 30 10	*37	Colour cathode-ray tube with a slit mask, having a distance between stripes of the same colour of less than 0,39 mm and a diagonal measurement of the screen of 33 cm or more but not exceeding 38 cm	0
ex 8540 30 90	*26	Flat screen monochrome cathode-ray tube, with a diagonal measurement of the screen of 142 mm or more but not exceeding 190 mm, a luminescence of 300 lumen or more but not exceeding 2 000 lumen, a resolution of 0,06 mm or more but not exceeding 0,1 mm, phosphor types P1 or P22 or P53 or P55 or P56, an anode voltage of more than 34 kV, a focus voltage of more than 7 kV and a cathode current of 3 mA or more	0
ex 8540 30 90	*27	Monochrome cathode-ray tube with a diagonal measurement of the screen of 310 mm or more but not exceeding 390 mm	0
ex 8540 89 11	*91	Displays in the form of a tube consisting of a glass housing mounted on a board the dimensions of which do not exceed 300 x 350 mm excluding leads. The tube contains one or more rows of characters or lines arranged in rows, each character or line consisting of fluorescent or phosphorescent elements. These elements are mounted on a metallised base which is covered with fluorescent substances or phosphorescent salts which give off light when bombarded with electrons	0
ex 8540 91 00	*91	Deflector yoke for cathode-ray tubes with an operating frequency of 31 250 Hz or more but not exceeding 64 000 Hz, incorporating a quadripolar magnet	0
ex 8540 91 00	*92	Slit mask, consisting of vertical slits with a distance between slits of 0,74 mm (±0,12 mm) and a diagonal dimension of either 61,5 cm (±0,5 cm) or 71 cm (±0,5 cm) or 79,5 cm (±0,5 cm)	0
ex 8540 91 00	*93	Electron gun for the production of monochrome cathode-ray tubes with a diagonal measurement of the screen of 7,6 cm or more but not exceeding 30,5 cm (a)	0
ex 8540 91 00	*94	Deflector yoke for colour cathode-ray tubes, with an operating frequency of 15 625 or 31 250 Hz, comprising two 2-pole ring magnets, two 4-pole ring magnets and two 6-pole ring magnets	0
ex 8540 91 00	*96	Assembly for cathode-ray tubes with 2 or more but not more than 6 coils, a platic support and a metal fixing ring, for the adjustment of display sharpness and/or convergence	0
ex 8540 99 00	*91	Anode, cathode or output part, or an assembly comprising these components (magnetron core tube), for the manufacture of magnetrons of subheading 8540 41 00 (a)	0
ex 8541 10 91	*30	Zener diode for overvoltage suppression, having a voltage of 24 V or more but not exceeding 30 V and with a dissipation rate of 5 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 2101DE or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 10 99	*30	Current regulative diode, providing a stabilized current level not exceeding 18 mA at a voltage of 10 V	0
ex 8541 10 99	*40	Diode, with a forward current not exceeding 1 A, a resistance not exceeding 1,5 ohm, a total capacitance not exceeding 0,3 pF and a breakdown voltage of 200 V or more	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 21 90	*10	High electron mobility transistor (HEMT), for frequencies of 2 GHz or more but not exceeding 20 GHz, with a dissipation rate not exceeding 180 mW, contained in a housing with a diameter not exceeding 3 mm, with not more than 4 connections	0
ex 8541 21 90	*20	Field-effect transistor (FET) for frequencies of 2 GHz or more but not exceeding 16 GHz, with a dissipation rate not exceeding 225 mW, contained in a housing with a diameter not exceeding 3 mm, with not more than 4 connections	0
ex 8541 29 20	*10	Field-effect transistor (FET), for frequencies of 2 GHz or more but not exceeding 10 GHz, with a dissipation rate not exceeding 6,5 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ATF 44101 ATF 46101 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*15	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 450 V or more, operating with a continuous drain-current not exceeding 18 A, a drain-to-source resistance not exceeding 0,4 ohm, and with a dissipation rate not exceeding 80 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 2SK1916 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*20	Field-effect transistor (FET) of the P-channel type, having a drain-to-source breakdown-voltage of –200 V, operating with a continuous drain-current not exceeding –1,8 A, a drain-to-source resistance not exceeding 3 ohm, and with a dissipation rate not exceeding 20 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: IRF 9610 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*25	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 500 V or more, operating with a continuous drain current not exceeding 1 A, a drain-to-source resistance not exceeding 5 ohm and with a dissipation rate not exceeding 40 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MTD1N50E or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*30	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 600 V or more, operating with a continuous drain-current not exceeding 6,2 A, a drain-to-source resistance not exceeding 1,2 ohm, and with a dissipation rate not exceeding 125 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: IRFBC40 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 29 20	*35	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 55 V or more, operating with a drain-to-source current of 0,9 A or more but not exceeding 3 A, a drain-to-source resistance not exceeding 1,5 ohm, and with a dissipation rate not exceeding 38 W, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1003SEDA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*40	Field-effect transistor (FET) of the P-channel type, having a drain-to-source breakdown-voltage of -30, -60 or -100 V, operating with a continuous drain-current not exceeding -9,6 A, a drain-to-source resistance not exceeding 0,28 ohm, and with a dissipation rate not exceeding 125 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IRF 9540 IRFU 9024 MMSF3P03HD or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*50	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 30 V or more, operating with a continuous drain-current not exceeding 6 A, a drain-to-source resistance not exceeding 0,05 ohm, and with a dissipation rate not exceeding 50 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MMSF5N03HD SMD30N03 SMU30N03 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*60	Field-effect transistor (FET) of the P-channel type, having a drain-to-source breakdown-voltage 60 V or more, operating with a continuous drain-current not exceeding 8,5 A, a drain-to-source resistance not exceeding 0,3 ohm, and with a dissipation rate not exceeding 30 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IRFD 014 IRFR 014 IRLR 014 IRFR 010 IRFU 014 IRLU 014 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 20	*70	Field-effect transistor (FET) of the N-channel type, having a drain-to-source breakdown-voltage of 60 V or more, operating with a continuous drain current not exceeding 35 A, a drain-to-source resistance not exceeding 0,1 ohm and with a dissipation rate not exceeding 125 W, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 5101FK (IRCZ24) 5101GK or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 29 80	*10	Transistor with a power of 150 W or more at a voltage of 160 V or more and with a cut-off frequency of 20 MHz or more, contained in a housing the exterior dimensions of which do not exceed 22 x 37 mm, with not more than 3 connections and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 2 SA 1170 2 SA 1494 2 SC 2921 2 SA 1215 2 SC 2774 2 SC 3858 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 80	*20	Transistor with thermal overload protection, having a collector-emitter operating voltage not exceeding 42 V, contained in a housing with not more than 4 connections	0
ex 8541 29 80	*30	Transistor with an output power not exceeding 30 W at a voltage of 12,5 V, contained in a housing with not more than 8 connections	0
ex 8541 29 80	*40	Transistor, having a dissipation rate not exceeding 250 W, a collector-emitter breakdown voltage of 120 V or more and a peak collector current not exceeding 40 A, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 2SC3675 2SC3886A 2SC4152 2SC3781 2SC3997 2SC4288 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 29 80	*50	Field-effect transistor (FET) of gallium arsenide (GaAs) semiconductor material, operating at a frequency of 2 GHz or more but not exceeding 18 GHz, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: NE76084 NE8004 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8541 40 19	*10	Light-emitting diode (LED), made from gallium-arsenic-phosphor (GaAsP) semiconductor material, operating at a nominal wavelength of 710 nanometers, in the form of a monolithic integrated circuit not contained in a housing (chip), for the manufacture of optocouplers (a)	0
ex 8541 40 19	*20	Light-emitting diode (LED), made from aluminium-gallium-arsenic (AlGaAs) semiconductor material, having a square base with an edge length not exceeding 8,2 mm, having a primary lens	0
ex 8541 40 19	*30	Light-emitting diode (LED) of Transparent Substrate (TS) technology, made from aluminium-gallium-arsenic (AlGaAs) semiconductor material, having a luminous intensity of 1,4 candela or more at 20 mA	0
ex 8541 40 93	*91	Photocouple, comprising a phototransistor with a collector current not exceeding 20 mA and a collector-emitter breakdown voltage of 30 V or more, and a light-emitting diode (LED) with a reverse current not exceeding 100 µA at a reverse voltage of 5 V, contained in a housing	0
ex 8541 60 00	*91	Piezo-electric crystal oscillating at a frequency of 32 768 Hz, contained in a cylindrical housing of a length not exceeding 8,2 mm and a diameter not exceeding 3,2 mm, for the manufacture of products falling within Chapter 91 (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8541 60 00	*92	Polarised ceramic piezo–electric crystal oscillating in a frequency range of 500 kHz or more but not exceeding 12 500 kHz, contained in a housing the exterior dimensions of which do not exceed 14 x 15 mm, with not more than 3 connections	0
ex 8541 60 00	*95	Ceramic filter and resonator elements for frequencies not exceeding 35 MHz, made of polarised piezoceramic, only equipped with electrodes or electrode patterns	0
ex 8542 11 01	*11	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers of C–MOS technology, with a processing capacity of 8 bits, providing servo control functions, comprising a read only memory, non–programmable (ROM) with a storage capacity of 128 Kbits, 2 random–access memories (RAMs) with a total storage capacity of 3 Kbits and a timer unit, for use in the manufacture of goods of subheading 8542 11 52 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 78134 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*12	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers of C–MOS technology, with a processing capacity of 16 bits, comprising a read only memory, non–programmable (ROM) with a storage capacity of 48 Kbits, a read only memory, non–programmable (ROM) with a storage capacity of 16 Kbits and a random–access memory (RAM) with a storage capacity of 4 Kbits, for use in the manufacture of goods of subheading 8542 11 53 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 77C25 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*25	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers of C–MOS or N–MOS (including H–MOS) technology, with a processing capacity of 16 bits, comprising a read only memory, non–programmable (ROM) with a storage capacity of 510 x 13 bits, a read only memory, non–programmable (ROM) with a storage capacity of 512 x 23 bits and a random–access memory (RAM) with a storage capacity of 2 Kbits, for use in the manufacture of goods of subheading 8542 11 53 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations figures or of figures and letters: 7720 77C20 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*30	Wafer, not yet cut into chips, consisting only of static random–access memories (S–RAMs) with a storage capacity of 1 Mbit, for use in the manufacture of goods of subheading 8542 11 25 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 431 000 or — other identification markings relating to devices complying with the abovementioned description (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 01	*40	Wafer, not yet cut into chips, consisting only of microprocessors, for use in the manufacture of goods of subheading 8542 11 43, 8542 11 45, 8542 11 47 or 8542 11 49 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 7810 78C10 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*50	Wafer, not yet cut into chips, consisting only of microcontrollers or microcomputers with a processing capacity of 16 bits, comprising a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or an UV erasable, programmable, read only memory (EPROM) with a storage capacity not exceeding 256 Kbits and one or more random-access memories (RAMs) with a total storage capacity not exceeding 12 Kbits, for use in the manufacture of goods of subheading 8542 11 53 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 78C11 78C14 78CP14 78C12 78C18 8XC196KT or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*70	Wafer, not yet cut into chips, consisting only of display controllers and character generators (DCCG), for liquid-crystal dot-matrix display systems, for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: 7228 7229 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 01	*80	Wafer, not yet cut into chips, consisting only of a programmable logic device, for use in the manufacture of goods of subheading 8542 11 61 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XC 2018 XC 3030 XC 3090 XC 4004 XC 4008 XC 4016 XC 2064 XC 3042 XC 4002 XC 4005 XC 4010 XC 4020 XC 3020 XC 3064 XC 4003 XC 4006 XC 4013 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*10	Control and driver circuit for dot-matrix liquid crystal displays (LCDs) of C-MOS technology, with a character generator, and having a drive voltage of more than 10 V, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of liquid crystal display (LCD) modules (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 05	*15	Control and interface circuit of BiMOS technology, capable of controlling communication between a microprocessor, bus control circuits and a memory control circuit, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G7432 16G7433 50G6759 50G6761 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*20	Driver circuit for liquid crystal displays (LCDs) of C-MOS technology, with 40 or more output channels, and having a drive voltage of more than 5 V, in the form of a monolithic integrated circuit not contained in a housing (microchip), for use in the manufacture of: — liquid crystal displays (LCDs), or — assemblies destined for LCDs (a)	0
ex 8542 11 05	*25	Bus control circuit of BiMOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G7429 16G7430 50G6755 50G6757 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*35	Memory control circuit of BiMOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 20 80 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G7428 16G7463 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*45	Bus control circuit of C-MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 2782654 52G7386 63F4073 63F4170 69G1705 52G7385 63F4057 63F4074 63F4378 81889051 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*50	Microprocessor, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 43, 8542 11 45, 8542 11 47 or 8542 11 49 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 32G7577 61F3516 8186364 82F5698 PC 601 PC 620 32G7578 80501 8188937 82F5700 PC 603 486 8186362 8188939 89G2847 PC 604 or — other identification markings relating to devices complying with the abovementioned description (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 05	*55	Memory control circuit of C–MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 32G7468 50G6870 51G8186 8186387 32G7567 50G8191 8184091 8188905 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*60	Triple digital–to–analogue video converter with 3 random–access memories (RAMDACs) of C–MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 69 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: RGB525 RGB530 (8187135) RGB528 RGB561 (8186987) or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*65	Bus interface and control circuit of C–MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 2782454 42G0929 61G2276 8184079 8184095 42G0928 51G8187 71G3184 8184093 8184108 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*70	Data/address buffer circuit of C–MOS technology, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 69 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: 2782653 8190694 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*75	Static random–access cache memory (Cache S–RAM) of C–MOS technology, with a storage capacity of 256 or 512 Kbits, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 23 or 8542 11 25 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 32G7587 32G7607 50G7244 8186377 32G7588 32G7608 8186318 8186379 or — other identification markings relating to devices complying with the abovementioned description (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 05	*80	Control circuit of C–MOS technology, providing local area network and memory control, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 85F7196 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 05	*85	Interface and control circuit of C–MOS technology, providing scan control and clock control, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 56 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 86G8155 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 12	*01	Dynamic random–access memory of N–MOS (including H–MOS) technology (N/H–MOS D–RAM) with a storage capacity of 64 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: KM 4164 MN 4264 TMS 4164 TMS 4416 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 12	*11	Dynamic random–access memory of N–MOS (including H–MOS) technology (N/H–MOS D–RAM), with a storage capacity of 256 Kbits and an access timenot exceeding 150 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HB 50562 KM 41257 MB 81464 PD 41256 TMS 4256 HM 50256 M5M 4256 MSM 4256 PD 41464 TMS 4464 HM 50464 M5M 4464 MSM 4464 TMM 41256 KM 41256 MB 81256 PD 41254 TMM 41464 or — other identification markings relating to devices complying with the abovementioned description	8
ex 8542 11 12	*12	Dynamic random–access memory of C–MOS technology (C–MOS D–RAM), with a storage capacity of 256 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 53 C 256 53 C 466 MB 81 C 466 TC 51832 53 C 258 HM 65256 P 51 C 256 53 C 464 MB 81 C 258 P 51 C 259 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 12	*13	Dual port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity of 256 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M5M 4 C 264 MB 81461 PD 41264 TMS 4461 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 14	*02	Dual or triple port dynamic random-access memory (D-RAM), with data registers and a serial read output control, with a storage capacity exceeding 256 Kbit but not exceeding 1 Mbit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M5M 442256 MT 42 C 4256 MT 43 C 8128 TC 528126 MB 81 C 4251 MT 43 C 4257 TC 524256 TC 528128 MSM 54C864 MT 43 C 4258 TC 524257 TMS 44 C 251 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 14	*05	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 64 K x 16 bit and an access time not exceeding 100 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TC511664BFT or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 16	*01	Pseudo-static random-access memory of C-MOS technology (C-MOS PS-RAM), with a storage capacity of 4 Mbits and an access time not exceeding 100 ns, comprising a timing pulse generator and a refresh control circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HM 658512 or — other identification markings relating to devices complying with the abovementioned description These devices are for the manufacture of portable computers, capable of operating without an external source of power (a)	0
ex 8542 11 16	*05	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity exceeding 1 Mbit but not more than 4 Mbits and an access time not exceeding 35 ns, comprising one or more static random-access cache memories (S-Cache-RAMs), in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 16 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: DM 2200 DM 2202 DM 2203 DM 2212 DM 2213 DM 2233 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 16	*06	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 2 Mbits and an access time not exceeding 60 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: V53C8256 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 16	*08	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 512 K x 8 bits and an access time not exceeding 100 ns, operating with a supply voltage of 3,3 V (±0,3 V), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HM51W4 800 (74G1307) (70G6821) or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 18	*01	Dynamic random-access memory of C-MOS technology (C-MOS D-RAM), with a storage capacity of 256 K x 18 bits and an access time not exceeding 80 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: PD 424280 or — other identification markings relating to devices complying with the abovementioned description	10
ex 8542 11 21	*01	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 16 x 4 bits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY27S03 CY54S189 CY7C189 CY27S07 CY74S189 CY7C190 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*02	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 2 K x 8 bits and an access time not exceeding 20 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IDT 6116LA20 IDT 6116SA20 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21 ex 8542 11 23 ex 8542 11 25 ex 8542 11 27	*12 *27 *27 *27	Static random-access memory (S-RAM), superimposed bit-for-bit on an electrically erasable, programmable, read only memory (E^2PROM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: STK 1390 STK 11C48 STK 14C68 X 2201 A X 2444 STK 1391 STK 11C68 X 2001 X 2210 X 20C16 STK 10C48 STK 11C88 X 2002 X 2212 STK 10C68 STK 13C68 X 2004 X 2443 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*16	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 16 Kbits, comprising a 5-bit bidirectionnal in-/output port and an universal asynchronous receiver/transmitter (UART), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 75012SC121 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*17	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 256 x 4 bits and an access time not exceeding 60 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C122 CY7C123 CY93422 CY93L422 P4C 422 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*19	Quadruple port static random-access memory of C-MOS technology (C-MOS quadruple port S-RAM), with a storage capacity not exceeding 64 Kbits and an access time not exceeding 45 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IDT 7050 IDT 7052 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*28	Static random-access memory (S-RAM), with a storage capacity of 1 K x 4 bits and an access time not exceeding 25 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 9150-20 CY 7 C 150-15 AM 9150-25 CY 7 C 150-25 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 21	*32	Static random-access memory of N-MOS (including H-MOS) technology (N/H-MOS S-RAM), with a storage capacity of 8 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 4008 4801 8108 8114 PD 421 4118 8104 8112 8185 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21 ex 8542 11 23 ex 8542 11 25 ex 8542 11 27	*37 *21 *12 *03	Non-volatile static random access memory of C-MOS technology (C-MOS non-volatile S-RAM), with an internal energy source, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BQ 4011 BQ 4024 MK 48 Z 02 MKI 48 Z 02 BQ 4013 BQ 4025 MK 48 Z 12 MKI 48 Z 12 BQ 4014 DS 1 220 Y MK 48 Z 30 BQ 4015 DS 1230 MK 48 Z 32 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*42	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 32 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: IDT 7134 TC 5532 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*43	Dual port static random-access memory of BiMOS technology (BiMOS dual port S-RAM), with a storage capacity not exceeding 64 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B134 CY7B135 CY7B139 CY7B1342 CY7B138 CY7B144 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 21	*48	Static random-access cache memory of C-MOS technology (C-MOS S-Cache-RAM), with a storage capacity of 4 K x 16 bits and an access time not exceeding 55 ns, comprising a 12-bit address latch and control circuits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: IDT71586S or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 23	*01	Static random-access cache memory of BiMOS technology (BiMOS S-Cache-RAM), with a storage capacity of 72 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B180 CY7B181 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*02	Dual port static random-access memory of BiMOS technology (BiMOS dual port S-RAM), with a storage capacity of 72 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CY7B145 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*03	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 192 Kbits and an access time not exceeding 35 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MCM 56824 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*19	Static random-access cache memory of C-MOS technology (C-MOS S-Cache-RAM), with a storage capacity of 144 Kbits and an access time not exceeding 30 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 55187 TC 55188 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 23	*20	Dual port static random-access cache memory of C-MOS technology (C-MOS dual port S-Cache-RAM), with a storage capacity of 256 Kbits, comprising a bus-memory control circuit, a bus control circuit of a central processing unit (CPU), an address-latch and 2 multiplexers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 82490XP or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 23	*23	Static random access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 32 K x 8 bits, a standby current not exceeding 25 µA and an access time exceeding 55 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXK 58257 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*01	Static random-access memory of BiMOS technology (BiMOS S-RAM), with a storage capacity of 288 Kbits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7B173 CY7B174 PD 46730 TC 55B329 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*08	Static random-access cache memory of BiMOS technology (BiMOS S-Cache-RAM), with a total storage capacity of 320 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combination of figures and letters: PD 46710 PD 46741 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 25	*11	Static random-access memory of C-MOS technology (C-MOS S-RAM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 551632 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 27	*05	Static random-access memory of BiMOS technology (BiMOS S-RAM), with a storage capacity of 64 K x 18 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MCM 67A618 MCM 67C618 or — other identification markings relating to devices complying with the abovementioned description	0
8542 11 31		Read only memory, non-programmable (ROM)	0
ex 8542 11 33	*02	UV erasable, programmable, read only memory (EPROM) of N-MOS (including H-MOS) technology (N/H-MOS EPROM), with a storage capacity not exceeding 256 Kbits, in the form of a monolithic integrated circuit contained in a housing, with a quartz window on the upper surface, and bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 33 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: M 2716 M 2732 A M 2764 M 27128 M 27256 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 33	*11	UV erasable, programmable, read only memory (EPROM), with a storage capacity not exceeding 64 Kbits, in the form of a monolithic integrated circuit contained in a housing, with a quartz window on the upper surface, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 27 CX 321 CY 7C 244 W CY 7C 263 W CY 7C 291 W 27 CX 322 CY 7C 245 W CY 7C 268 W CY 7C 243 W CY 7C 261 W CY 7C 269 W or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 33 ex 8542 11 41	*16 *16	UV erasable or non–erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 128 Kbits and an access time not exceeding 100 ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 7C 251 CY 7C 254 XC 17128 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 33	*24	UV erasable, programmable, read only memory (EPROM) of C–MOS technology (C–MOS EPROM), with a storage capacity of 16 K x 16 bits, comprising a 5–bit gate only for reading, in the form of a monolithic integrated circuit contained in a housing, with a quartz window on the upper surface, and bearing: — an identification marking consisting of or including the following combination of figures and letters: N 73120FRN or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 33 ex 8542 11 41	*51 *21	UV erasable or non–erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 256 Kbits and an access time not exceeding 65 ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 27C256–55 CY 7C 270 CY 7C 275 CY 7C 279 27H256–35 CY 7C 271 CY 7C 276 XC 17256D 27H256–45 CY 7C 274 CY 7C 277 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 34	*05	UV erasable, programmable, read only memory (EPROM) of C–MOS technology (C–MOS EPROM), with a storage capacity of 448 Kbits and an access time not exceeding 170 ns, in the form of a monolithic integrated circuit contained in a housing, with a quartz window on the upper surface, and bearing: — an identification marking consisting of or including the following combination of figures and letters: N 70413FEC–A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 34	*12	UV erasable, programmable, read only memory (EPROM) of N–MOS (including H–MOS) technology (N/H–MOS EPROM), with a storage capacity of 512 Kbits, in the form of a monolithic integrated circuit contained in a housing, with a quartz window on the upper surface, and bearing: — an identification marking consisting of or including the following combination of figures and letters: M 27512 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 34 ex 8542 11 41	*24 *24	UV erasable or non–erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits, comprising address and data latches, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 87C510 TMS 87PC510 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 34 ex 8542 11 41	*27 *27	UV erasable or non–erasable, programmable, read only memory (EPROM or PROM), with a storage capacity of 512 Kbits and an access time not exceeding 55 ns, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 7C 286–50 CY 7C 287–45 CY 7C 287–55 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 34 ex 8542 11 41	*51 *51	UV erasable or non–erasable, programmable, read only memory (EPROM or PROM) with a storage capacity of 1 Mbit, comprising address and data latches, in the form of a monolithic integrated circuit contained in a housing, with or without a quartz window on the upper surface, and bearing :	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 34 ex 8542 11 41 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: TMS 87C110 TMS 87PC110 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*03	Electrically erasable, programmable, read only memory (E^2PROM) of C–MOS or N–MOS (including H–MOS) technology, capable of byte–wide (parallel) write operations, with a storage capacity of 2 K × 8 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: X 2816C X 28 C 16 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*61	Flash electrically erasable, programmable, read only memory (Flash–E^2PROM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 29 F 816 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 38	*71	Flash electrically erasable, programmable, read only memory (Flash–E^2PROM) with a storage capacity of 896 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: N71564FTCHCA or — other identification markings relating to devices complying with the abovementioned description	0.
ex 8542 11 40	*02	Ferroelectric memory of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: FM1208S or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*21	FIFO (first in, first out), read/write memory of C–MOS technology, with a storage capacity of 64 × 8 bits or 64 × 9 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C408A CY7C409A or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 40	*25	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 2 K x 18 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C447 CY7C457 MT 53C2K18C3 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*26	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal, with a storage capacity of 512 x 9 bits and an access time not exceeding 40 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 4601 Am 7201-35 CY7C451 Am 7201-25 CY7C441 IDT 72211 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*36	FIFO (first in, first out) read/write memory, with a storage capacity of 7 280 or 9 080 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PD 41101 PD 41102 PD 42101 PD 42102 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*39	FIFO (first in, first out) read/write memory of C-MOS technology, for simultaneous reading and writing in one clock signal or bidirectional operation, with a storage capacity of 9 or 18 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 7202A CY7C446 IDT 72031 Am 7203A CY7C453 IDT 72215 CY7C439 CY7C455 MT 53C51218A1 CY7C443 CY7C456 CY7C445 IDT 72021 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*43	FIFO (first in, first out) read/write memory of C-MOS technology, with a storage capacity of 72 Kbits and with an access time not exceeding 15 ns or a programmable flag register, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 7205-15 CY7C470 MT 53C4K18D4-15 CY7C460-15 IDT 7205 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 40	*55	FIFO (first in, first out) read/write memory of C–MOS technology, with a storage capacity of 144 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C462 CY7C472 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 40	*65	FIFO (first in, first out) read/write memory of C–MOS technology, with a storage capacity of 288 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C464 CY7C474 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 41	*25	Programmable, non–erasable, read only memory (PROM) of C–MOS technology (C–MOS PROM), with a storage capacity not exceeding 64 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY7C225 CY7C249 CY7C268 HM 6616 XC 1764 CY7C235 CY7C258 CY7C269 MB 7143 XC 1765 CY7C243 CY7C259 CY7C281 MB 7144 CY7C244 CY7C261 CY7C282 MB 71C44 CY7C245 CY7C263 CY7C291 XC 1718 CY7C248 CY7C264 CY7C292 XC 1736 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 41	*66	Memory register of C–MOS technology, with a storage capacity of 1 Mbit, comprising an address generator providing serial read/write operation, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MSM 6389 or — other identification markings relating to devices complying with the abovementioned description	0
8542 11 43 8542 11 45 8542 11 47 8542 11 49		Microprocessors	0
8542 11 51		Microcontroller or microcomputer with a processing capacity not exceeding 4 bits	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52	*03	Microcontroller or microcomputer of N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, having peripheral interface functions, comprising a random-access memory (RAM) with a storage capacity not exceeding 2 Kbits, a read only memory, non-programmable (ROM), a programmable, non-erasable, read only memory (PROM) or a UV erasable, programmable, read only memory (EPROM) with a storage capacity of 16 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: 8042 8742 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*05	Microcontroller or microcomputer of N-MOS (including H-MOS) technology, with a processing capacity of 8 bits, comprising an UV-erasable, programmable, read only memory (EPROM) with a storage capacity of either 14 432 bits, 30 016 bits or 30 208 bits, a random-access memory (RAM) with a storage capacity of either 832 bits or 896 bits, a read only memory, non-programmable (ROM) with a storage capacity of either 920 bits, 1 528 bits or 1 728 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 68705P3 MC 68705R3 MC 68705S3 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*07	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, having a register-to-register architecture, comprising a static random-access memory (S-RAM) with a storage capacity of not more than 12 Kbits and at least a read only memory, non-programmable (ROM) or a programmable, non-erasable, read only memory (PROM) or an UV-erasable, programmable, read only memory (EPROM) or an electrically erasable, programmable, read only memory (E^2PROM), with a storage capacity of not more than 256 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 370C010 370C250 370C732 73C85 370C032 370C256 370C756 73C88 370C050 370C310 370C758 73C95 370C052 370C332 370C810 73C161 370C056 370C350 370C850 MC 68HC05P1 370C058 370C352 374C036 MC 68HC05P8 370C150 370C356 73C41 370C156 370C358 73C42 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52	*25	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, for text data decoding and display, comprising a read only memory, non-programmable (ROM) with a storage capacity of 8 Kbits, a read only memory, non-programmable (ROM) with 120 character fonts and a random-access memory (RAM) with a storage capacity not exceeding 2 304 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CF 72307 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*27	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing vertical deflection functions for a cathode-ray tube, comprising 2 arithmetic-logic units (ALUs), 4 read only memories, non-programmable (ROMs) with a total storage capacity of 11,7 Kbits, 2 random-access memories (RAMs) with a total storage capacity of 1 Kbit, an analogue-to-digital converter and 2 digital-to-analogue converters, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD 2018 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*31	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing keyboard control functions, comprising a read only memory, non-programmable (ROM) with a storage capacity of 2 Kbits, random-access memories (RAMs) with a total storage capacity of 2 Kbits, a real-time clock, address registers and input/output buffers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 82C113 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*33	Microcontroller or microcomputer of C-MOS technology, with a processing capacity of 8 bits, providing remote control functions, comprising a read only memory, non-programmable (ROM) with a storage capacity not exceeding 128 Kbits and a random-access memory (RAM) with a storage capacity not exceeding 4 Kbits, in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MN 187164 PCA 84C222 PCA 84C822 PCA 84C122 PCA 84C422 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52	*37	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, providing voice message storage, comprising a read only memory, non–programmable (ROM) with a storage capacity of 128 Kbits, an UV–erasable, programmable, read only memory (EPROM) interface circuit, a random–access memory (RAM) interface circuit and a communication interface circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: D6305 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*39	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, providing communication and control functions in local operating networks (LONs), comprising three 8–bit central processing units (CPUs), a static random–access memory (S–RAM) with a storage capacity not exceeding 16 Kbits and an electrically erasable, programmable, read only memory (E^2PROM) with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 143120 MC 143150 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*41	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, providing protocol control, data formatting and audio signal processing, comprising 1 or 2 random–access memories (RAMs) with a total storage capacity not exceeding 10 Kbits and a read only memory, non–programmable (ROM) with a storage capacity of 192 Kbits, in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: Am 79C412 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*55	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, comprising a random–access memory (RAM) with a storage capacity of 2 or 8 Kbits, an electrically erasable, programmable, read only memory (E^2PROM) with a storage capacity of 4 Kbits and an 8–channel analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 68HC11A1 SC 415111FU SC 415016FU TMP 68HC11A1 MC 68HC11F SC 415112FU SC 805666FN or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 52	*85	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, comprising a 16–bit digital signal processor, a random–access memory (RAM) with a storage capacity of 4 Kbits or more but not exceeding 16 Kbits and having the function of programme memory, 2 random–access memories (RAMs) with a total storage capacity of 2 Kbits or more but not exceeding 8 Kbits and 256 registers, in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Z 86294 Z 86295 Z 86C95 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 52	*87	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 8 bits, providing floppy disc storage unit or keyboard control functions, comprising an 8–bit configuration register, a random–access memory (RAM) with a storage capacity of 16 Kbits and having the function of programme memory, a random–access memory (RAM) with a storage capacity of 2 Kbits and a real–time clock, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters : PC 87323 VF PC 87911 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*04	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 19 bits, providing audio functions and transmit/receive functions of a digital cordless telecommunication system, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 79C420 SC 14400 SC 14401 SC 14420 SC 14460 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*12	Microcontroller or microcomputer of N–MOS (including H–MOS) technology, with a processing capacity of 32 bits, comprising 24 registers of 32 bits and a random–access memory (RAM) with a storage capacity of 2 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HGC 6127 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*19	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 32 bits, comprising 16–bit data–buses and a 16 x 16 bits multiplier with 32 bits results, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ST 18930 ST 18940 ST 18942 TMS 320M520 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 54	*21	Microcontroller or microcomputer with a processing capacity of 32 bits and a 16–bit data–bus, comprising random–access memories (RAMs) with a total storage capacity not exceeding 450 Kbits, one or more read only memories, non–programmable (ROMs) or one or more UV erasable, programmable, read only memories (EPROMs) with a total storage capacity not exceeding 768 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 320 10 320 C 17 320 C 53 TMS 320C59 320 11 320 C 25 320 E 15 320 C 10 320 C 50 320 E 17 320 C 15 320 C 51 TMS 320C25A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*25	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 32 bits, comprising a read only memory, non–programmable (ROM) with a storage capacity of 4 Mbits, a random–access memory (RAM) with a storage capacity of 1 Mbit, a display control and drive circuit, an interrupt controller, a keyboard controller, a memory mapping control circuit and a clock generator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SC 414181FG16 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*31	Microcontroller or microcomputer with a processing capacity of 32 bits, comprising one or more random–access memories (RAMs) with a total storage capacity not exceeding 48 Kbits, a read only memory, non–programmable (ROM) with a storage capacity not exceeding 128 Kbits and a floating point arithmetic unit with a capacity of 32 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DSP 32 MB 86232 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*36	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 32 bits, comprising one or more random–access memories (RAMs) with a total storage capacity of 64 Kbits and a read only memory, non–programmable (ROM) with a storage capacity not exceeding 128 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 320 C 30 320 C 40 DSP 3207 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 54	*41	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 32 bits, consisting of a system integration module (SIM) with configuration register, a random–access memory (RAM) with a storage capacity of 16 Kbits, a time processor unit (TPU) with 16 independent programmable channels, a serial peripheral interface and a serial communications interface, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 68332 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 54	*51	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of 32 bits, having the function of audio–data processing, comprising a multiplier/accumulator (MAC) of 52 bits, 2 dynamic random–access memories (D–RAMs) with a total storage capacity of 12 Kbits and 2 static random–access memories (S–RAMs) with a total storage capacity of 14 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMC 57000 TMC 57001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 55	*51	Microcontroller or microcomputer of C–MOS technology, with a processing capacity of more than 32 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 4920 DSP 56 000 DSP 56002 DSP 9 6002 DSP 1616 DSP 56001 DSP 56166 TMS 320C500 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*02	Printer control circuit of C–MOS technology, comprising a dynamic random–access memory (D–RAM) control circuit, direct memory access (DMA) registers, registers for communication between 2 microprocessors and a static random–access memory (S–RAM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TMX 35C438 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*05	Display controller or character generator for liquid crystal displays (LCDs), light–emitting diodes (LEDs) or fluorescent displays, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: D 16302 ECN 2102 HD 61830 MC 141540 TC 9240F D 16306 ECN 2112 LH 5821 PD 16311 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56	*07	Disc storage unit controller, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 0391343 CL SH250 MB 89311 1454-001 CL SH260 OMTI 505 6006Z1 CL SH265 OTI 018 6008 CL SH350 OTI 033 600B CL SH360 PD 7261 61156-001 CL SH361 PD 7262 61157-001 CL SH362 WD 1010 74G7202 DP 8473 WD 16C92 82077 FDC 37C665 WD 2010 8980 FDC 37C666 WD 37C65 ADS 10C00 FE 2100 WD 57C65 AIC 010 G 70360-33 WD 42C22 AIC 100 HDC 9224 WD 5010 AIC 610 L HDC 9234 WD 5011 AIC 6190 HDL 33AHQ120 WD 76C20 AIC 65 C 10 B HG 62S04L02F Z 86 C 99 AIC 8265 L 1 A 0519 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*12	Control and/or management circuit for memories (including buffers), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 0404 1872 82 C 283 MC 68440 1RJ3-0001 82 C 302 MC 68450 1TU9-0301 82 C 325 MC 68851 390 Z 55 82 C 392 NS 32082 68451 A 38202 NS 32382 82307 CY7C604 T 9490 82357 CY7C605 THCT 4502 82359 GC 113 TX 32082 W 82380 GC 183 VC 2730-0001C 82385 HD 68450 VL 4502 82395 HDL 33A115-00GX WD 1015 82424 TX HDL 33A116-00GX WD 11 C 00-22 82485 HDL 3N120-00HQ WD 12 C 00-22 82495 XP HG 62S079L25F WD 83 C 580 82 C 08 HT 113 WD 83 C 583 82 C 102 HT 322 WE 32104 82 C 222 HT 342 Z 8516 82 C 223 L1A4599 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*14	Serial and/or parallel communication controller, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82 050 HDL 32Y108-00HQ SCN 68562 82 C 605 HDL 3N119HQ119 SCN 68652 82 C 606 MC 2652 SCN 68661 82 C 607 MC 68652 WD 76C30 Bt 8701A PD 7 2001 Z 80 C 30 CL-CD180 SCN 2652 Z 85 C 30 HDL 32Y107-00HQ SCN 2661 Z 85 C 35 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56	*15	Digital line interface control circuit of C–MOS technology, capable of controlling the data flow between a system interface circuit, a subscriber line interface circuit (SLIC) and a microprocessor interface circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TP 3120 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*18	Control circuit of C–MOS technology, providing memory control, comprising an equiliser, a memory interface circuit, a multiplexer, a decoder, a bus control circuit and direct memory access (DMA) control circuits in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1TX6–0301 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*19	Control circuit of C–MOS technology, operating at 12 MHz, comprising a programmable interval timer, a clockgenerator, two direct memory access controllers and a memory mapper, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 82231 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*22	Control circuit of C–MOS technology, for the management of asynchronous cycles of a 32–bit central processing unit (CPU), of a direct memory access (DMA) circuit and of a multimaster bus, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters : 82 C 321 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*24	Control circuit or control and management circuit, comprising 2 direct memory access (DMA) controllers and 2 interrupt controllers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82360SL 82C491 HT 101 SX 82C206 82C593 VL 82 C 480 82C316 GC 101 SX VL 82 C 486 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56	*25	Control circuit of C–MOS technology, for controlling and interfacing signals between a central processing unit (CPU), memory and input/output interfaces, comprising circuits for refreshing dynamic random–access memories (DRAMs), for decoding of addresses, for generating clock signals and monitoring data transfer interrupt signals, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 344 S 0602 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*27	Control circuit of C–MOS technology, for a microcontroller, a microcomputer or a microprocessor with a processing capacity of 16 or 32 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82C311 TACT 82S411 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*29	Timing control unit (TCU) with two–phase cycle for central processing unit (CPU) and memory management unit (MMU), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: NS 32201 NS 32 C 201 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*31	Control circuit of C–MOS technology, capable of driving 25 lamps or a 7–segment light–emitting diode (LED) display, having a drive voltage of 4,5 V or more but not exceeding 6 V, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 14489 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*34	Circuit for connecting/disconnecting buses, of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MCCS142233 MCCS142234 MCCS142235 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*36	Control and interface circuit of C–MOS technology, comprising a memory controller, a peripheral controller, a central processing unit (CPU) interface circuit, a numeric processor unit (NPU) interface circuit, a clock generation circuit, a timer and a parity–check circuit, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: HT 15 HT 18 HT 21 HT 22 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*37	Interface or interface and control circuit of C–MOS technology, with at least one of the following functions: a) for signals between a peripheral disc memory unit and a central processing unit (CPU), b) for controlling data communication between a system bus interface and peripheral units, comprising a system interface gate, a microprocessor gate and a direct memory access (DMA) gate, c) for interfacing and controlling the data sequence between an automatic data–processing machine and a disc storage unit, d) for read/write data between a digital–audio–tape storage unit and a microprocessor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) 0391374 a) QS 3384 a) 68G0960 a) WD 11 C 00–17 a) 68G1002 a) WD 14 C 00–17 a) 82C611 a) WD 61 C 40 a) AIC 560 L b) 1TU1–0301 a) DP 8466 b) 1TU2–0301 a) M 5213 b) 1TV3–0301 a) M 5215 b) 1TV3–0302 a) OMTI 5080 (OMTI 2 0508) b) 1TV4–0301 a) OMTI 5090 (OMTI 2 0509) b) 1TV4–0302 a) QS 32383 c) 32C260 a) QS 32384 c) AIC 6060 a) QS 3383 d) 1XK2–0301 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*40	Control and interface circuit of C–MOS technology, capable of receiving, processing and transmitting subscriber data in a digital network, comprising a line interface unit, a multiplexer, a data link controller, a microprocessor interface and an oscillator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 79C30A AM 79C32A QMV 453 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*41	Control and interface circuit based on gate arrays of C–MOS technology, capable of controlling and interfacing between an inkjet printer, a dynamic random–access read/write memory (D–RAM) and microprocessors, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1TY6–0001 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56	*43	Control and interface circuit for central processing unit (CPU) of C–MOS technology, comprising a control unit for the refreshment of memories, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: FE 3010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*46	Control and interface circuit of C–MOS technology, comprising 48 mA drivers, registers, an 18– or 32–bit direct memory access (DMA) interface, an 8– or 32–bit microprocessor bus and a parity generator and checker, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM53C974 NCR 53C94 NCR 53C95 NCR 53C96 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*48	Interface and control circuit of C–MOS technology, comprising 2 universal asynchronous receiver/transmitters (UARTs), a parallel–data port, a hard–disc interface and a floppy–disc controller, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 37C665 37C666 82C711 TACT 88511 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*55	Dual or octal universal asynchronous receiver/transmitter (Dual or octal UART), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 1TQ1–0202 2681 PC 87310 SCC 2698 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*65	Circuit for data transfer between a microprocessor and memory cards of a thickness of 3 mm or more, of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MB 86301 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*67	Programmable asynchronous communication element circuit of C–MOS technology, for the asynchronous transmission and reception of data, comprising a FIFO (first in, first out) read/write memory and at least one serial input/output channel and a bi–directional parallel channel, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: 16C551 16C552 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*68	Programmable interval timer/counter of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 82C54 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*69	Computing unit of C-MOS technology, without an internal programme sequencer for the multiplication or processing of fixed and floating point numbers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 3210 ADSP 3220 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*71	8 x 8 bit- or 16 x 16 bit-multiplier or multiplier/accumulator (MAC) of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADSP 1008-A CY7C510 CY7C516 CY7C517 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*73	Message handler circuit based on gate arrays of C-MOS technology, providing multi-channel communication over a bidirectional bus, comprising a microprocessor interface circuit, a voice/data receiver and transmitter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 253 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*82	Interface and control circuit based on standard cells of C-MOS technology, capable of data organisation and transfer to an inkjet printhead, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1MK9-0201 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 56	*84	Digital signal synthesiser based on standard cells of C–MOS technology, comprising 32 independent programmable channels, a clock generator, an input/output decoder, a microprocessor with a processing capacity of 8 bits, 2 timers, an interrupt controller, 2 digital–to–analogue converters and an analogue–to–digital converter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VY 06243 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*87	Audio control circuit of C–MOS technology, providing tone control, comprising a voice suppression circuit, a mute circuit, a surround control circuit and a microprocessor serial interface circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: M62423FP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*88	Audio signal processing circuit of C–MOS technology, providing sound effects generation, comprising one or more random–access memories (RAMs), an adder and a microprocessor interface, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 8905 CS 9203 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*89	Multiplexer/demultiplexer of C–MOS technology, comprising a microprocessor bus interface circuit and a random–access memory (RAM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 03301 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 56	*90	Data–buffer or data/address–buffer circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82C592 82C602 FB 2020 HT 102 82C601 90G9465 GC 102 VL 82 C 332 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*01	Delay circuit of C–MOS technology, comprising one static random–access memory (S–RAM) with a storage capacity of 8 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 57 *(cont'd)*		— an identification marking consisting of or including the following combination of figures and letters: M50198P or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*04	Control circuit of C–MOS technology, for the firing of printhead pens, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters : 1TY5–0001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*08	Flow meter interface of BiMOS technology, comprising 16 amplifiers, 3 digital–to–analogue converters, an analogue–to–digital converter, filters, a sample and hold circuit, an oscillator, a phase locked loop (PLL) circuit and a serial interface circuit for a microprocessor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD75027 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*12	Interface circuit of C–MOS technology, for a keyboard with a capacitive matrix, capable of matrix scanning and detection, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 22–00958–000 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*15	Encoder/decoder with filter of C–MOS technology, for frequencies of 600 Hz or more but not exceeding 4 000 Hz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 112 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*20	Quadruple encoder/decoder with pulse–code–modulation filters of C–MOS technology, comprising amplifiers for sidetone balance, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 365 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 57	*25	Digital–to–analogue and analogue–to–digital converter of BiMOS technology, comprising sample and hold circuits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 21–26500 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*27	Circuit of BiMOS technology, for the recording and reproduction of data, operating at a rate not exceeding 112 Mbits/sec, comprising an encoding circuit, a decoding circuit, an analogue–to–digital converter, a digital equaliser filter and a random–access memory (RAM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 64G0166 (8189294) or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*31	Synchronising circuit combined with a scan and signal distributor of C–MOS technology, comprising a control unit, a contact bounce elimination circuit, a 17–bit shift register and a data output formatting unit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: QMV 222 or — other identification marking relating to devices complying with the abovementioned description	0
ex 8542 11 57 ex 8542 11 69	*44 *66	Data or image compression/decompression circuit of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 1XH4–0301 1XY9–0001 CL 450 CL 950 1XK6–0301 74 ACT 6340 CL 550 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 57	*52	Circuit of C–MOS technology, providing synchronisation and discrimination of read–signals and generation of write signals, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HG 22SS013601 or — other identification marking relating to devices complying with the abovementioned description	0
ex 8542 11 58	*05	Gate array of C–MOS technology, with metal gates, with an operating voltage of 12 V, with not less than 637 two–input functions, having within the array a digital code produced by an electron beam, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 58 *(cont'd)*		— an identification marking consisting of or including the following combination of figures and letters: FB 215 or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 11 60	*10	Audio signal processing circuit based on standard cells of C–MOS technology, comprising a read only memory, non–programmable (ROM), a random–access memory (RAM), 4 analogue–to–digital converters, a serial interface, a frequency decimation circuit and a loudspeaker overload protection circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VY 27051 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 60	*20	Audio digital filter based on standard cells of C–MOS technology, with 16 channels, each of them real–time programmable with 20 parameters or more, comprising a multiplier/accumulator (MAC), a timer and 2 random–access memories (RAMs) for the storage of parameters and of temporary processing data, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VC 5396 or — other identification markings relating to devices complying with the abovementioned description	0
8542 11 61		Programmable logic device	0
ex 8542 11 63	*04	8–bit identity comparator of C–MOS technology, with a propagation delay of not more than 7,2 ns, capable of bit for bit comparison of 2 words of not more than 8 bits each, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 74FCT521 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*07	Address comparator of C–MOS technology, comprising one or more static random–access memories (S–RAMs), a parity generator, a parity checker and one or more comparators, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SN 74 ACT 2151 SN 74 ACT 2154 SN 74 ACT 2158 SN 74 ACT 2152 SN 74 ACT 2156 SN 74 ACT 2159 SN 74 ACT 2153 SN 74 ACT 2157 SN 74 ACT 2160 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 63	*14	Flip-flop of the D-type of C-MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74 AC 16821 74 AC 74 74 ACT 16374 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*17	Latch, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74ACT11373 74FCT16373 74FCT533 74FCT843 74FCT162373 74FCT16841 74FCT573 74FCT844 74FCT162841 74FCT373 74FCT841 74FCT845 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*19	Logic circuit of C-MOS technology, having only one of the following functions: — single or dual AND, — single or triple INVERTER, — single or dual NAND, — single or dual NOR, — exclusive, single or dual OR, — single or dual bilateral switch, — Schmitt-trigger, — 2-channel multiplexer, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 4S01F TC 4S71F TC 4W53F TC 7S08F TC 7W04F TC 4S11F TC 4S81F TC 4W66F TC 7S32F TC 7W08F TC 4S30F TC 4S584F TC 7S00F TC 7SU04F TC 7W32F TC 4S66F TC 4SU11F TC 7S02F TC 7W00F TC 7WU04F TC 4S69F TC 4SU69F TC 7S04F TC 7W02F or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*20	Logic circuit of C-MOS technology, having AND-, OR, INVERTER- and NAND-functions, comprising an 18 bit latch and a clock generation circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74G7350 PD65622 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*21	Transmitter of C-MOS technology, operating with a single power supply of +5 V and with a data transfer rate per second of 120 Kbits or more at an output voltage of 5 V, comprising not more than 5 transmitters, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 63 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: MAX 200 MAX 204 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*22	Differential line receiver of C–MOS technology, with a typical propagation delay not exceeding 19 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS34C86 DS34C87 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*27	Transmitter/receiver of C–MOS technology, with at least one of the following characteristics: a) of the 8–, 16– or 32–bit bidirectional type, b) operating with a single power supply of +5 V and a supply current not exceeding 2 mA, comprising 2 transmitters and 2 receivers, c) operating with a single power supply of +5 V, comprising at least 6 transmitters and at least 8 receivers, d) operating with a single power supply of +5 V and of +9 V or more but not exceeding +13,2 V and with a data transfer rate per second of 120 Kbits or more at an output voltage of 5 V, comprising 2 transmitters and 2 receivers, e) having a supply current not exceeding 3 mA, comprising 2 driver circuits and 3 receivers, f) with a propagation delay not exceeding 22 ns, g) with a data transfer rate per second of 256 Kbits, a typical quiescent current of 120 µA and a propagation delay not exceeding 30 ns, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: a) 29 C 833 A a) 74 FCT 162652 c) MAX 246 a) 29 C 853 A a) 74 FCT 162952 c) MAX 247 a) 74 AC 16472 a) 74 FCT 16500 c) MAX 248 a) 74 AC 16646 a) 74 FCT 16501 c) MAX 249 a) 74 AC 16657 a) 74 FCT 16543 d) MAX 201 a) 74 ACT 11245 a) 74 FCT 16646 e) LTC 902 a) 74 ACT 16245 a) 74 FCT 16652 f) 29 FCT 52 a) 74 ACT 16470 a) 74 FCT 16952 f) 82503 a) 74 ACT 16863 a) 74 FCT 245 f) DS7517BT a) 74 FCT 1 62500 a) 74 FCT 645 g) MAX 483 a) 74 FCT 1 62501 b) MAX 220 g) MAX 487 a) 74 FCT 1 62543 c) MAX 244 g) MAX 488 a) 74 FCT 162646 c) MAX 245 g) MAX 489 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 63	*41	Buffer and line driver of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 54 ACT 16244 74 AC 16827 74 FCT 241 74 FCT 541 74 AC 16240 74 ACT 16244 74 FCT 244 74 AC 16825 74 FCT 240 74 FCT 540 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 63	*51	Translator of BiMOS technology, to convert to emitter–coupled logic (ECL) levels or to transistor–transistor logic (TTL) levels, capable of bus driving of full duplex data transmission, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY10E383 CY101E383 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*04	Remote control circuit of C–MOS technology, capable of generating 2 048 different commands and controlling 32 systems, comprising a keyboard encoder, a keyboard decoder, a parallel to serial converter, a divider, a reset generator and an oscillator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SAA 3010 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*06	8–channel control circuit of C–MOS technology, for maintaining a constant electromagnetic traction force with incorporated diodes and a storage capacity of 8 bits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: UCN 5801 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*09	Monolithic integrated circuit with at least 16 analogue switching elements, of C–MOS technology, for controlling signals of 20 Hz or more but not exceeding 20 000 Hz, capable of dealing with signals up to 3 V with a distortion of not more than 0,05 % over the whole frequency range at a voltage of 1 V, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TC 9164 N TC 9177 P TC 9184 P or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*13	DC motor control circuit, with at least one of the following caracteristics: a) for a brushless three–phase motor, of BiMOS technology, operating at a single power supply of 5 V, comprising outputs with an impedance of 2,3 ohms at a power of 1 A, b) of C–MOS technology, comprising a circuit to monitor power supply, a circuit to store and decode addresses and to multiplex data, an 8–bit digital–to–analogue converter and 5 amplifiers, c) of N–MOS (including H–MOS) technology, comprising a digital 16–bit filter,	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 65 *(cont'd)*		in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) PRD 1029 b) GC 27 b) GC 45 c) LM 629 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*15	Control circuit of C–MOS technology, capable of processing read–signals and of controlling the motor of a compact–disc player, comprising a central processing unit (CPU) interface, an error detection/correction circuit, a read–signal demodulator, a phase locked loop (PLL) circuit and a constant–linear–velocity (CLV) controller, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXD 1125 CXD 1130 CXD 1135 CXD 1167 MN 66271 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*19	Controller for servo–devices of C–MOS technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: KM 3702 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*21	Control circuit of C–MOS technology, capable of controlling video–signals of a charge–coupled (CCD) image sensor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXD 2103 CXD 2133 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*22	Audio control circuit of C–MOS technology, capable of 2–channel (stereo) volume control, comprising a multiplexer, 2 amplifiers, a control register and a serial–to–parallel register, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CS 3310 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*25	Control circuit of C–MOS technology, for a microprogramme, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CY 2910 CY 7C 910 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 65	*28	Control circuit, of C–MOS technology, for monitoring the voltage of random–access memories (RAMs) in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BQ 2201 BQ 2202 BQ 2204 BQ 2502 BQ 2503 DS 1210 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*31	Line decoder/driver of C–MOS technology, with an output voltage of 30, 35 or 60 V at 500 mA, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 34142 UCN 5816 UCN 5817 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 65	*35	Control circuit of C–MOS technology, capable of managing the reduction of power consumption of a microprocessor or of other peripheral units, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters : 1028 CP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 66	*05	Pulse–code–modulation line interface circuit of C–MOS technology, comprising a line transmitter, a line receiver, an encoder/decoder, a jitter attenuator, a clock and data recovery circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 61574 CS 61575 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 66	*11	Interface circuit of C–MOS technology, for at least one encoder, capable of identifying and measuring direction and displacement via signals of external sensors, comprising at least 3 counters, at least one latch of 16 or 24 bits, at least one multiplexer, at least one 8–bit parallel data buffer, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: THCT 2 000 THCT 12016 THCT 12024 THCT 12316 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 66	*13	Interface circuit for a text data decoder of C–MOS technology, capable of data–slicing, clock regeneration and synchronisation separation, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 66 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: CF 72303 CF 72306 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 66	*16	Interface and control circuit of C–MOS technology, programmable, for interfacing signals between video–graphic–array (VGA) controllers and cathode–ray tube (CRT) displays, liquid crystal displays (LCDs), light–emitting–diode (LEDs) displays or plasma–displays, capable of simultaneously controlling a CRT–display and a LCD display, comprising a digital–to–analogue video–converter with random–access memory (RAMDAC), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CL–GD6340 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 66	*21	Repeater interface and control circuit of C–MOS technology, comprising 7, 8 or 12 transmission/reception interface ports, an attachment–unit interface (AUI) port and a phase locked loop (PLL) decoder, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: Am 79C980 Am 79C981 DP 83950 DP 83955 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*02	8–bit data–synchroniser of C–MOS technology, capable of transmitting and receiving serial or parallel data at a rate of 155,52 Mbits or 19,44 megabytes per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TXC 02301 TXC 02302 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*05	Universal synchronous receiver/transmitter of C–MOS technology (C–MOS USRT), capable of full duplex digital voice and/or data transfer with a speed of 80 Kbits/s or more over a distance of 2 km or a speed of 160 Kbits/s or less over a distance of 1 km, comprising a modulator and data buffers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 145421 MC 145425 TP 3401 TP 3403 MC 145422 MC 145426 TP 3402 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*06	Transmitter/receiver of C-MOS technology, with at least one of the following characteristics: a) capable of connecting (terminating) line rates of 8 448 or 34 368 Kbits per second, b) for signals between an encoder/decoder using Manchester code (MED) or an interface unit and a twisted pair cable or a coaxial cable in a local area network (LAN), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: a) TXC 02 050 b) 83C94 b) MC 145572 b) 83C92 b) Am 79C98 b) TMS 380C60 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*07	Dual-tone multi-frequency (DTMF) receiver of C-MOS technology, capable of decoding DTMF signals to 4-bit binary data, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: M-957 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*09	Serial/parallel converter of C-MOS technology, capable of driving displays, in the form of a monolithic intergrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HV 5122 HV 5306 HV 5406 HV 7708 HV 5222 HV 5308 HV 5408 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*11	Digital-to-analogue and analogue-to-digital converter of C-MOS technology, comprising an analogue modulator capable of oversampling signals at a frequency of 1 024 MHz and a filter capable of sampling signals from a digital modulator at a frequency of 512 kHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MSP 58C20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*13	Analogue-to-digital signal converter, comprising amplifiers, digital-to-analogue and analogue-to-digital converters with a supply voltage of 12 V (±10 %) and a digital serial interface with an asynchronous receiver/transmitter, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 75002 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*16	Sampling rate converter of C–MOS technology, capable of converting a clock signal with a frequency of 13,5 MHz or more but not exceeding 18 MHz into a clock signal with a frequency of 18 MHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD 2032 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69 ex 8542 11 99	*19 *69	Disc storage unit data separator (DDS), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DP 8460–2 DP 8465 VM 5352 WD 10 C 21 DP 8460–3 HDC 9226 VM 5353 DP 8460–4 VM 5351 WD 10 C 20 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*24	Signal processing circuit of C–MOS technology, providing delay of scanning periods for horizontal image lines of a charge–coupled (CCD) image sensor, comprising a clockgenerator, a clamp circuit and a sample and hold circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXL 1517 MN 3860SA MSM 6819MS–K CXL 5504 MN 3861SA MSM 6834MS–K or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*25	Digital signal synthesiser of C–MOS technology, with at least one of the following characteristics: a) comprising random–access memories (RAMs) with a total storage capacity of 16 Kbits, with a sampling rate of 22,257 kHz and 44,1 kHz and 2 output channels, b) comprising 32 or 48 frequency generators, a clock generator and an address generator, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) 344 S 0053 b) VC 2375 b) VC 5395 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*26	Frequency synthesiser of BiMOS technology, capable of synchronising and dividing of frequencies, comprising 1 or 2 phase–locked loop circuits and 1 or 2 prescalers with an operating frequency of 10 MHz or more but not exceeding 2,5 GHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MB 1501 MB 1502 MB 1509 MB 1511 MB 1518 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*27	Signal generator of C–MOS technology, providing synchronous pulse generation for a charged coupled (CCD) image sensor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXD 1030 CXD 1217 LZ 93B53 LZ 93N43 LZ 95G52 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*28	Signal processing circuit of C–MOS technology, capable of processing video–signals from a charge–coupled (CCD) image sensor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1810 CXD 2100 CXD 2150 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*30	Phoneme speech synthesiser of C–MOS technology, with a supply current of less than 10 mA, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 78 A 263 CD 54123 N2L CM 54145 N2L CD 54121 N2L CD 54147 N2L CM 54146 N2L CD 54122 N2L CM 54104 CM 54166 N2L or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*32	Video processing circuit of C–MOS technology, providing aspect ratio conversion and interlace conversion for luminance/chrominance signals, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD 2035 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*33	Encoder/decoder of C–MOS technology, capable of encoding, decoding and interfacing serial signals having a rate of 13 Kbits per second and audio signals having a rate of 104 Kbits per second, comprising an analogue–to–digital converter, a digital–to–analogue converter, digital–pulse–code–modulation filters and an echo cancellation circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VP 22020 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*34	Encoder/decoder of BiMOS technology, providing data conversion and separation and a data transfer rate of 50 Mbits per second, comprising a read pulse detector and a frequency synthesiser/synchroniser, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69 *(cont'd)*		— an identification marking consisting of or including the following combination of figures and letters: HD 153031 RF or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*35	Decoder of C–MOS technology, for demodulating and demultiplexing of stereo signals, comprising an interface circuit of a digital–to–analogue converter having an output clock signal of 8,192 or 16,384 MHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CF 70088 CF 70091 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*37	Encoder/decoder of C–MOS technology, for the conversion of data into NRZ (Non–Return–to–Zero) format or RLL (Run–Length–Limited) format, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 61158 CL–SH110 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*38	Audio decoder of C–MOS technology, capable of decoding and demultiplexing audio signals and digital data, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 8411 CS 8412 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*39	Adaptive differentiated pulse–code–modulation circuit of C–MOS technology, for encoding/decoding speech and data and capable of full and half duplex data–transfer, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BBSP4CH Bt 8110 SC 11360 SC 11362 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*41	Audio encoder of C–MOS technology, capable of encoding and multiplexing audio signals and digital data, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 8401 CS 8402 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*45	Encoder/decoder of N-MOS (including H-MOS) technology, for the conversion of data into serial or parallel signals, comprising an arithmetic logic unit (ALU) and a read only memory, non-programmable (ROM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 38020 TMS 38021 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*46	Phase-locked loop (PLL) clock circuit of C-MOS technology, capable of synchronisation or multiplication of frequencies not exceeding 160 MHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 74 FCT 3888915 MC 88915 MC 88920 74 FCT 88915 MC 88916 MC 88PL117 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*47	Clock recovery circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DP 83231 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*48	Clock/calendar circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 58274 MC 146818 MM 58174 A V 3023 M 3002 MCCS 146818 V 3021 M 3003 MM 58167 V 3022 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*50	Address generator of C-MOS technology, for the address generation of a source image and a target image during image manipulation, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TMC 2302 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*53	Delineation circuit of C-MOS technology, capable of extracting and inserting asynchronous transfer mode (ATM) cells from and into a line interface signal, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 05150 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*57	Modulator/demodulator of C-MOS technology (C-MOS-Modem), only for half duplex transfer of image telegraphy (facsimile) at a rate of 300, 2 400, 4 800, 7 200 or 9 600 bits per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TC 35128 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*58	Modulator/demodulator of C-MOS technology (C-MOS-Modem), for full duplex data-transfer at a rate not exceeding 2 400 bits per second and for half duplex transfer of image telegraphy (facsimile) at a rate not exceeding 9 600 bits per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SC 11044 SC 11046 SC 11054 SC 11055 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*60	Read channel circuit, providing read/write and servo demodulator functions, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32P4730 91C020 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*63	Generator of C-MOS technology, for a user-definable cursor, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: Bt431 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69 ex 8542 11 99	*65 *65	Smoke detector operating in a temperature range of –20 °C or more but not exceeding 60 °C, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 14467 MC 14471 CS 235 MC 14468 MC 145010 V 24216 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*68	Video-line comb filter of C-MOS technology, capable of digital signal luminance/chrominance separation, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXD 2024 CXD 2030 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 69	*70	Digital filter of C–MOS technology, capable of quadruple or octuple over–sampling for 2 channels, comprising a read only memory, non–programmable (ROM) and a random–access memory (RAM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TMS 57021 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*74	Echo and reverberation module, comprising a multiplier/accumulator, two random–access memories (RAMs) and a read only memory, non–programmable (ROM), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VC5344 VC5909 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*75	Digitally controlled potentiometer of C–MOS or N–MOS (including H–MOS) technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SC 76013 X 9104 X 9313 X 9C103 X 9102 X 9311 X 9503 X 9C104 X 9103 X 9312 X 9C102 X 9C503 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*77	Hall effect sensor of BiMOS technology, capable of communicating over a 2–wire bus, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: UGN 3055U UGS 3055U or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 69	*79	8 x 16–bit differential crosspoint switch of C–MOS technology, capable of switching at a frequency of 20 MHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MT 8816 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 70	*01	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of clock and data recovery circuits, for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 70 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: GD 16042 GD 16043 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 70	*04	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of multiplexer circuits, capable of multiplexing 4 data flows into a single data flow, comprising a phase–locked loop (PLL) circuit and laser diode drivers, for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 16054 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 70	*07	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of transmitter/receivers, providing serial data communication at a rate of 622 Mbits per second, for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 16064 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 70	*11	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of dual buffers for ECL/TTL level signals, for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 10225 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 70	*15	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of multiplexers or demultiplexers, providing differential ECL level data input/output at a rate of 622 Mbits per second and TTL input/output signals at a rate of 78 Mbits per second, for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: GD 16131 GD 16132 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 71	*01	Measurement circuit of gallium arsenide (GaAs) semiconductor material, capable of measuring signal propagation times on transmission lines, comprising 2 asynchronous counters, 4 comparators, a clock generator and an oscillator, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 71 *(cont'd)*		— an identification marking consisting of or including the following combination of letters: GIGA TDR or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 71	*04	Clock and data recovery circuit of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: GD 16042 GD 16043 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 71	*11	Transmitter/receiver of gallium arsenide (GaAs) semiconductor material, providing synchronous/asynchronous data communication at a rate per second of 622 Mbits or more but not exceeding 2,5 Gbits, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of letters: GIGA BOA GIGA MATCH or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 71	*15	Multiplexer of gallium arsenide (GaAs) semiconductor material, capable of multiplexing 4 data flows into a single data flow, comprising a phase-locked loop (PLL) circuit and laser diode drivers, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 16054 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 71	*21	Divider/detector circuit of gallium arsenide (GaAs) semiconductor material, capable of synthesizing frequencies in the range of 50 MHz to 1 700 MHz, comprising a prescaler, a frequency divider and a phase/frequency detector, in the form of a monolithic integrated circuit not contained in a housing (chip), for use in the manufacture of goods of subheading 8542 11 99 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GIGA FSS or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 11 73	*10	Static random-access memory (S-RAM) of gallium arsenide (GaAs) semiconductor material, with a storage capacity not exceeding 4 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 73 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: 12G014 12G044 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 73	*21	Static random-access memory of bipolar technology (bipolar S-RAM), with a storage capacity not exceeding 1 Kbit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 82 S 09 93422 93425 MBM 93419 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 76	*31	Programmable, non-erasable, read only memory (PROM) of bipolar technology, with a storage capacity not exceeding 64 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 18 S 030 27 S 191 5309 6336 82 S 130 24 S 10 27 S 291 53 S 240 63 S 240 82 S 131 24 S 41 28 L 22 53 S 241 63 S 241 93436 24 S 81 28 LA 22 54 S 570 63 S 3281 93446 28 L 42 28 L2XMFC 54 S 571 7053 Am 27S43 28 S 166 29613 5604 7058 MB 7115 28 S 42 29770 5624 74 S 570 MB 7116 28 S 86 29771 6305 74 S 571 MB 7117 27 PS 191 38510 6306 76 LS 03 MB 7118 27 PS 291 5305 6308 7620 MB 7141 27 S 12 5306 6309 7621 MB 7142 27 S 13 5308 6335 82 S 114 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 76	*35	Programmable, non-erasable, read only memory (PROM) of gallium arsenide (GaAs) semiconductor material, with a storage capacity of 4 Kbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 14GD048 14GM048 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 83	*04	Error correction and detection unit (ECDU) of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 2960 74 AS 632 74 F 630 74 LS 631 54 AS 632 74 AS 634 74 F 631 DP 8400 54 AS 634 74 AS 6364 74 LS 630 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 83	*05	Control and/or management circuit for memories (including buffers) of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 2965 DP 8409 DP 8429 AM 2966 DP 8419 MB 1422 DP 8408 DP 8428 SN 74 S 409 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 83	*09	Control circuit for disc storage units of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AmMPA 1850 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 83	*14	Controller of bipolar technology, for controlling read/write signals from magnetic heads in disc storage units, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters : 32 R 2020 R 32 R 510 A 32 R 522 32 R 2021 R 32 R 5121 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 83	*18	Bus interface circuit of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 82 A 203 82 A 304 AM 29821 AM 29825 AM 29845 82 A 204 82 A 305 AM 29822 AM 29826 RYT 121 82 A 205 82 A 436 AM 29823 AM 29843 82 A 303 82 A 442 AM 29824 AM 29844 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 83	*24	Analogue–digital monolithic integrated circuit of bipolar technology for interface signals between the hard–disc, memory unit and the central processing unit (CPU), contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 581 C or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 85	*05	Control circuit of TTL technology, for the firing of magnetic print hammers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: 801379–002 810751–001 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 93	*10	Programmable logic device, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or figures and letters: 16L4 20R6 CY10E302 SN 54 LS 334 16L6 20R8 CY100E301 SN 54 LS 335 16L8 22V10 CY100E302 SN 54 LS 336 16R4 82 S 100 FP 54 AS 839 SN 74 LS 333 16R6 82 S 101 FP 54 AS 840 SN 74 LS 334 16R8 93458 FP 74 AS 839 SN 74 LS 335 20L8 93459 FP 74 AS 840 SN 74 LS 336 20R4 CY10E301 SN 54 LS 333 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*05	Flip-flop of the D-type of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combination of figures and letters: F100131 F100151 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*10	Latch of the D-type of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: F100150 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*11	Register/latch of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TD62C948 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*15	Counter/shift register of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: F100136 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*20	8-bit shift register of bipolar technology, with emitter-coupled logic (ECL) input/output, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: F100141 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 94	*25	Translator of bipolar technology, to convert to emitter–coupled logic (ECL) levels or to transistor–transistor logic (TTL) levels, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 57–240245 F100124 F100125 MB 768 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 94	*26	Transmitter/receiver of bipolar technology, operating from a single power supply of +5 V, with a data transfer rate of 120 Kbits or more per second at an output voltage of 5 V, comprising not more than 5 transmitters and not more than 5 receivers, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1130 LT 1180A LT 1237 LT 1381 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 96	*05	Control circuit of bipolar technology, capable of driving laser diodes or other light–emitting diodes (LEDs), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: IDA 07318 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 96	*10	Driver circuit of gallium arsenide (GaAs) semiconductor material, for controlling laser diodes or other light–emitting diodes (LEDs), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G075 16G076 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 96	*15	Control circuit of bipolar technology, capable of controlling 2 discrete power field–effect transistor (FET) devices, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 27473 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 96	*20	Driver circuit for write signals for magnetic tape storage units, of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VT 211 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 96	*30	Control circuit of bipolar technology, capable of driving a PNP power transistor, having a 5 V standby-power-regulation and a 2,5 V power output reference, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 7015 FB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 96	*35	Control circuit of bipolar technology, capable of driving 2 pulse-code-modulation lines at a transfer rate not exceeding 10 Mbits/s, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: XRT5675 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 97	*05	Interface and control circuit of bipolar technology, for interfacing signals between data processing machines and coaxial cable in a local area network (LAN), in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 7996 DP 8392 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 97	*10	Interface circuit for the synchronisation of data flow from a disc storage unit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DP 8462 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*03	Transmitter of bipolar technology, providing encoding/conversion of parallel data/commands into serial format, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 79168 AM 7968 AM 79865 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*04	Receiver of bipolar technology, providing decoding/conversion of serial data/commands into parallel format, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AM 79169 AM 7969 AM 79866 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 99	*05	Transmitter or receiver of bipolar technology, capable of serial data communication at a rate of 110 Mbits or more but not exceeding 1,4 Gbits per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HDMP 1002 HDMP 1004 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*07	Transmitter or receiver of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: GA 9011 GA 9012 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*10	Receiver/transmitter of Schottky technology, for Manchester–coded data, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TMS 38051 TMS 38053 TMS 38054 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*14	Pulse–code–modulation (PCM) transmitter/receiver of bipolar technology, capable of connecting (terminating) line rates of 2 048 or 8 448 Mbits per second, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XRT 5683 XRT 56L85 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*21	Audio digital–to–analogue converter of bipolar technology, with a dynamic range of 96 dB or more, comprising an internal voltage reference, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: PCM 63P or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*23	12–bit analogue–to–digital converter of bipolar technology, incorporating a voltage reference and clock, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 574 A or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 99	*25	9-bit analogue-to-digital converter of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TDC 1049 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*27	12-bit digital-to-analogue converter of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TDC 1012 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*29	Digital-to-analogue converter of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TQ 6122 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*33	16-bit digital-to-analogue converter of bipolar technology, comprising an internal voltage reference, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: DAC 712 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*41	Programmable amplifier of bipolar technology, for signals on a digital communications bus, in the form of monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HS 3182 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*43	Monolithic integrated circuit (read/write data processor circuit) for the amplification and conversion of read signals and conversion of write signals for hard-disc drives, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 32 P 3000 32 P 3013 32 P 540 32 P 541 61347-002 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 99	*46	Demodulator/tone–decoder of bipolar technology for frequency decoding, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: XR 2211 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*49	2–, 4–, 6– or 8–channel read/write signal generator for hard–disc drives, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32 R 117 32 R 501 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*51	Function generator of bipolar technology for the generation of variable wave–forms, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XR 2206 XR 8038 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*54	Data–synchroniser for tape–reading units of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: VT 210 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*56	Data synchroniser and encoder/decoder of bipolar technology, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or letters: 32 D 532 32 D 535 32 D 5393 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*61	Clock and data recovery circuit of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 16G040 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*63	Clock generator/buffer of gallium arsenide (GaAs) semiconductor material, capable of frequency synchronisation or multiplication, in the form of a monolithic integrated circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 11 99 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: GA 1 000 GA 1086 GA 1088 GA 1110 GA 1085 GA 1087 GA 1089 GA 1210 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*66	Comparator circuit of gallium arsenide (GaAs) semiconductor material, for phase and frequency differences of frequencies not exceeding 1 GHz, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 16G044 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*68	Digitise and data–separation circuit of bipolar technology, comprising a phase–locked loop circuit, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SN 28962 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*73	Differential crosspoint switch of bipolar technology, capable of switching at a data rate per second of 800 Mbits, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: S 2024 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*74	Decoder of bipolar technology, for chrominance signal decoding, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: M52725FP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 11 99	*75	Clock distribution circuit of bipolar technology, with inputs for transistor–transistor logic (TTL) signals or emitter–coupled logic (ECL) signals and outputs for transistor–transistor logic (TTL) signals, in the form of a monolithic integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 63G9269 MC 100H640 MC 100H644 MC 10H641 64G0112 MC 100H641 MC 10H640 MC 10H644 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 10	*10	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of transimpedance amplifiers, operating at a bandwidth of 900 MHz, having a resistance not exceeding 4 kohm, for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 16085 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 10	*20	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of laser diode control circuits, providing an output current in a range of 10 mA to 70 mA at a power supply of −5 V (±1 %), for use in the manufacture of goods of subheading 8542 19 70 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 16077 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 10	*30	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of amplifiers with a typical output power of 25 dBm in a frequency range of 1 850 MHz to 1 950 MHz, comprising radiofrequency (RF) switches, for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 12033 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 10	*40	Wafer, not yet cut into chips, of gallium arsenide (GaAs) semiconductor material, consisting only of dual amplifiers with a typical gain of 18 dB at a frequency of 1,5 GHz, for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GD 10012 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 10	*60	Wafer, not yet cut into chips, consisting only of amplifiers with an input current not exceeding 80 nA, for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: INA 101 OPA 111 OPA 121 OPA 2111 or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 10	*70	Wafer, not yet cut into chips, consisting only of amplifiers with a programmable gain factor, for use in the manufacture of goods of subheading 8542 19 30 contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 10 *(cont'd)*		— an identification marking consisting of or including the following combination of figures and letters: 3606G or — other identification markings relating to devices complying with the abovementioned description (a)	0
ex 8542 19 20	*10	Amplifier, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 20	*15	Amplifier of bipolar technology, for the amplification of read/write signals of thin film magnetic heads, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of disc storage units (a)	0
ex 8542 19 20	*20	FM receiver/amplifier of bipolar technology, in the form of an monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 20	*25	Audio recording/reproducing circuit of C–MOS technology, capable of direct analogue storage of audio data, comprising an electrically erasable, programmable, read only memory (E^2PROM), 3 amplifiers, an automatic gain control circuit and 2 filters, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of clocks and watches (a)	0
ex 8542 19 20	*30	Control circuit of C–MOS technology, capable of driving inductive and resistive loads, having 4 outputs with a current of 2 A or more but not exceeding 7,2 A, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for the manufacture of motor control systems (a)	0
ex 8542 19 20	*40	Transimpedance amplifier of gallium arsenide (GaAs) semiconductor material, operating at a bandwidth not exceeding 2,7 GHz, in the form of an monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of products falling within subheading 8527 11 99 (a)	0
ex 8542 19 20	*50	Differential amplifier of bipolar technology, with a gain not exceeding 375 and a nominal input voltage of 1 mVpp, in the form of a monolithic integrated analogue circuit not contained in a housing (chip), for use in the manufacture of products falling within heading No 8471 (a)	0
ex 8542 19 30	*02	Microwave amplifier of bipolar technology, with a nominal gain of 18 dB at 0,5 GHz or 32 dB at 0,9 GHz or 11 dB at 1 GHz or 22,5 dB at 1 GHz and 32,5 dB at 0,1 GHz or 26 at 1,5 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: A–06 HPMX 3002 N10 A–08 MSA 0311 PC 1652G or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*03	Quadruple amplifier of C-MOS technology, with an input current not exceeding 20 pA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LMC 660 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*05	Amplifier of bipolar technology, with a typical supply current not exceeding 1 mA at a voltage of 12 V and a temperature of 25 °C, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LM 1964 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*06	Amplifier of bipolar technology, with a typical operating frequency of 1,3 GHz, 2,3 GHz or 3 GHz and a single supply voltage of 5 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: C1D C1E C1F C1G C1H C1J or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*07	Amplifier with an off-set voltage not exceeding 10 mV at 25 °C, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1006 MC 33272 OPA 275 TLC2022 LT 1028 MC 33274 OPA 628 TLC27M2 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*10	Amplifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: C 05 V 35 or — other identification markings relating to devices complying with the abovementioned description These devices are for use in the manufacture of products falling within subheading 9021 40 00 (a)	0
ex 8542 19 30	*11	Transimpedance amplifier of bipolar technology, with a typical gain of 72,5 dB at a frequency of 750 MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: ITA 12318 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*12	Amplifier of gallium arsenide (GaAs) semiconductor material, operating within a frequency range of 824 MHz to 928 MHz and an output power of 0,794 W (29 dBm) or more but not exceeding 3,162 W (35 dBm), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AWT 0900 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*13	Amplifier with a typical gain of 10,5 dB at a frequency of 2 GHz and with an output power of 10 dBm (10 mW), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MAR 3SM or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*14	Video amplifier of bipolar technology, with a bandwidth of 200 MHz, comprising a contrast control circuit, a comparator and a voltage reference circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LM 1201 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*15	Amplifier with an input current not exceeding 80 nA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: INA 101 OPA 27 OPA 37 OPA 111 OPA 121 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*16	Video amplifier of bipolar technology, providing separate amplification of red, green and blue (RGB) colour signals, comprising at least a contrast control circuit and a comparator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HA 11533NT LM 1202 LM 1203 LM 1205 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*25	Variable amplifiers for the range of frequencies of 10 Hz or more but not exceeding 30 kHz, with a gain of 85 dB or more, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: M 5218 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 30	*40	Thermocouple amplifier for instrumentation control at temperatures from 0 to 50 °C, incorporating an alarm system, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 594 AD 595 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 30	*80	Amplifier with a programmable gain factor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: PGA 102 PGA 202 PGA 203 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*10	Voltage regulators with a quiescent current of 75 µA and a drop out voltage of 380 mV at 100 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LP 2950 LP 2951 MIC 2951 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*15	Current and voltage regulator, operating on a battery input voltage of 0,85 V or more but not exceeding 5,5 V or an unregulated input voltage of 7 V or more but not exceeding 20 V, providing a selectable output voltage of 3,3 V (±0,13 V) or 5 V (±0,20 V), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MAX 717 MAX 719 MAX 721 MAX 723 MAX 718 MAX 720 MAX 722 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*25	Voltage regulator, providing reverse battery protection, operating with an input voltage not exceeding 60 V and a quiescent current not exceeding 70 µA at zero load or a quiescent current not exceeding 30 mA at load current of 1 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LM 2940 LT 1129 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*30	Adjustable shunt voltage regulator, comprising an internal voltage reference and divider resistors with a collector (sink) current of 1 mA or more but not exceeding 100 mA and an initial voltage reference tolerance of 0,4 %, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1431 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 50	*35	Voltage regulator, with a quiescent current not exceeding 75 mA and a dropout voltage not exceeding 0,6 V at an output current of 750 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TL750M TL751M or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*40	Variable voltage regulator with a supply current not exceeding 120 µA at an output current not exceeding 100 µA and a dropout voltage not exceeding 0,85 V at an output current of 125 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1020 LT 1120 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*45	Voltage regulator, having an output voltage of 12 V (±3 %), a quiescent current not exceeding 10 mA and a dropout voltage not exceeding 22 V at an output current of 50 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CS 8109 (7032FB) or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*50	Voltage regulator with an output voltage of 2,1 V (±2,5 %) or 3 V (±2,5 %) at a nominal output current of 40 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 0 C (RH5 RA 30 AA) 1 B (RH5 RA 21 AA) or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*60	Voltage regulator with an input voltage range of 4,75 V or more but not exceeding 60 V and a quiescent current not exceeding 10 mA, comprising a 1 A switch circuit and an oscillator with a fixed frequency of 52 kHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LM 1575 LM 2575 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*65	Voltage regulator, having an output voltage of 1 V or more but not exceeding 8 V, a typical quiescent current of 400 or 500 µA, a typical dropout voltage of 170 mV at an output current of 60 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TK 114 (R3) TK 115 TK 116 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 50	*70	Voltage and current regulator of bipolar technology, capable of generating 3 ouput currents of respectively 7,5 mA, 50 mA and 750 mA at an ouput voltage of 5 V (±5 %), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 34 992 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*75	Voltage regulator, having an output voltage of 3,9 V (±3 %), a typical output current of 40 mA at an input voltage of 6 V and a typical operating current of 2,2 µA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SCI 7710Y-KA or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*80	Voltage regulator, with a dropout voltage not exceeding 1,5 V at an output current of 3 A or more but not exceeding 9,5 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LT 1083 LT 1084 LT 1085 LT 1585 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 50	*85	Voltage regulator, with an output current of 50 mA at a typical input to output differential voltage of 0,35 V or an output current of 30 mA at a typical input to output differential voltage of 0,15 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: S 8420 S 8850 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 61	*10	Quadruple fuel injector driver smartpower circuit of BiMOS technology, comprising a voltage regulator, an overvoltage detection circuit and an output status control circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 71 00050FSE or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 61	*20	Smartpower circuit, capable of controlling DC motors, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MPC 17A50VM or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 61	*40	Smartpower circuit, capable of power supply switching of memory cards, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MAX 780 MIC 2557 MIC 2558 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*10	Tachometer or tachometer and speedometer control circuit of BiMOS or bipolar technology, comprising a voltage regulating function, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 8190 T 8557G TB 9226N TB 9228N TB 9233N or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*15	Video control circuit of bipolar technology, capable of swithching YUV/RGB signals and controlling contrast, brightness and colour, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1839 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*20	Speedometer and odometer drive and control circuit, whether or not having amplification functions, comprising 4 frequency dividers, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TA 8906 TB 9207 TB 9208 TB 9212 TB 9230 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*25	Video control circuit of bipolar technology, providing control pulse generation for image recording, comprising an amplifier for write–signals and an amplifier for read–signals, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TA 8823 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*35	Disc storage unit controller of C–MOS or BiMOS technology, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 1323453 M52896FP (53G7897) 1SP9–0003 PD 16828 53G8800 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 65	*45	Control circuit of BiMOS technology, capable of switching video signals, with 3 video inputs, 3 control outputs and a buffer output, comprising a clamp circuit, in form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: BA 7021 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*50	Clock recovery circuit of bipolar technology, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 800 AD 802 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*55	Control circuit of BiMOS technology, capable of switching audio signals, with 5 audio inputs, 5 control outputs and 3 output buffers, in form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: BA 7632 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*60	Speedometer, tachometer or odometer drive and control circuit, comprising at least a digital–to–analogue converter and a multiplexer, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SA 5775 SA 5777 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*70	Temperature control circuit, with a temperature sensor and an internal voltage reference, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TMP 01 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 65	*80	Video control circuit of BiMOS technology, capable of driving a cathode–ray tube, providing horizontal/vertical deflection and colour signal processing, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1840 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 69	*11	Control circuit, capable of driving inductive or resistive loads, having an output current not exceeding 1,3 A at a supply voltage not exceeding 28 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 71004 SB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*13	Control circuit of BiMOS technology, capable of driving inductive and resistive loads having 4 outputs with a current of 2 A or more but not exceeding 7,2 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 100904 HIP 0082 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*21	Control circuit of bipolar technology, for driving DC motors with brushes, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BA 6109 BA 6209 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*23	DC motor control circuit of bipolar technology, providing an output current of 2 A at an output saturation voltage of 3,2 V, comprising 3 TTL inputs, 4 transistors in a full bridge configuration and an overvoltage shutdown circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 71004 MB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*25	Three-phase DC motor control circuit of BiMOS technology, comprising a Hall effect threshold detection circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 1323454 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*29	Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an supply voltage not exceeding 24 V and an operating temperature of –40 °C to +125 °C, comprising an overvoltage shutdown circuit and a thermal shutdown circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 69 *(cont'd)*		— an identification marking consisting of or including the following combination of figures: 34993 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*31	Circuit for driving linear motors or motors with rotating arms, of bipolar technology, working with an output voltage of 45 V at an output current of 1,75 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: UDN 2917 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*33	Brushless three-phase DC motor control circuit of bipolar technology, operating with an input current of 1 µA and having an input off-set current of 0,1 µA at an input off-set voltage of 5 mV, comprising a thermal shutdown circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HA 13490 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*39	Control circuit of bipolar technology, capable of driving solenoids, operating with a power supply current not exceeding 50 mA at a supply voltage not exceeding 7 V and a dissipation rate not exceeding 19 W, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 71008SB 71013SB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*41	Control circuit of C-MOS technology, for monitoring the voltage of microprocessors, microcontrollers or microcomputers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS 1231 H 6060 MN 13802 MN 13821C DS 1232 H 6061 MN 1381 MN 13822C H 6006 MN 1380 MN 13811 MN 1382C H 6052 MN 13801 MN 13812 V 7039 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*43	Voltage regulator control circuit, operating with a supply voltage of 6 V or more but not exceeding 30 V, providing an output voltage of 5 V (±0,1 V) at an output current of 220 µA, in form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1432 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 69	*54	Control circuit of C–MOS technology, capable of amplifying/inverting voltage levels to drive vertical lines of a charge–coupled (CCD) image sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXD 1267 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 69	*57	Control circuit of bipolar technology, capable of switching video and audio functions, comprising amplifiers and a mixer of luminance and chrominance signals, in form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CX 1545 CXA 1845 CXA 1855 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*10	Interface circuit of dielectric isolation technology, for telephone sets with a line voltage not exceeding 265 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LH 1497 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*20	Interface and control circuit of C–MOS technology, for the generation of graphic symbols on a cathode–ray tube, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MN 1297 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*50	Interface circuit of bipolar technology, capable of converting a differential input signal into a square wave output signal of the same frequency, comprising 4 signal sensor channels and a timer, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 71001AB or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 70	*60	Interface circuit or interface circuit with control functions, for a local area network (LAN), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SMC 83C805 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 70	*70	Video signals interface circuit of bipolar technology, capable of interfacing with a red, green and blue (RGB) colour signal circuit, comprising 3 automatic white balance adjustment circuits, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1 024S or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*05	Audio recording/reproducing circuit of C-MOS technology, capable of direct analogue storage of audio data, comprising an electrically erasable, programmable, read only memory (E^2PROM), 3 amplifiers, an automatic gain control circuit and 2 filters, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ISD 1012A ISD 1020A ISD 1210 ISD 2545 ISD 2575 ISD 1016A ISD 1 200 ISD 1400 ISD 2560 ISD 2590 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*08	Dual-tone multi-frequency (DTMF) generator of C-MOS technology, capable of decoding 4-bit binary data and generating 16 tone pairs, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TP 5088 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*17	Signal processing circuit of C-MOS technology, providing analogue signal filtering and gain control, comprising a dual-tone multifrequency (DTMF) transmitter and a DTMF receiver, and a modulator/demodulator (Modem), in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SC 11370 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*20	Audio signal processing circuit of bipolar technology, providing input/output mode selection, comprising 2 voltage controlled oscillators, 2 phase-locked loop (PLL) circuits, an automatic gain control circuit, an audio noise reduction circuit, a mute circuit and amplifiers, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LA 7481W or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 95	*24	Local telephone network circuit of C–MOS technology, capable of tone generation and of switching, amplifying and decoding audio signals from not more than 2 external telephone lines and from not more than 12 internal telephone lines, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SC 11390 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*27	Analogue communication circuit, capable of data conversion and signal transfer, comprising a serial input/output port for a digital signal processor (DSP), a 16–bit analogue–to–digital converter, a 16–bit digital–to–analogue converter and a clock generator, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 28MSP01 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*31	Encoder/decoder of C–MOS technology, for base–band and voice–band frequencies, providing data conversion, comprising a modulator for digital signals, analogue–to–digital converters, digital–to–analogue converters, amplifiers and filters, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 7015 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*33	16–bit stereo encoder/decoder with C–MOS technology, having sample rates of 4 kHz or more but not exceeding 48 kHz, comprising a multiplexer, a digital–to–analogue converter, an analogue–to–digital converter, a mute circuit, a voltage reference circuit, a microphone–input, a loudspeaker–output and a headphone–output, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 1849 CS 4215 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*37	Encoder/decoder with pulse–code–modulation filter of C–MOS technology, operating with a +5 V single–power supply, comprising an analogue–to–digital converter and a digital–to–analogue converter, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145480 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 95	*39	Encoder/decoder with pulse-code-modulation filter of C-MOS technology, with a dual-power supply and having a typical dissipation rate of 50 mW, comprising an analogue-to-digital converter and a digital-to-analogue converter, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 145503 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*42	Adaptive differentiated pulse-code-modulation circuit of C-MOS technology, for encoding/decoding data with a data transfer rate of 8, 16, 24, 32 or 64 Kbits per second, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: T 7 280 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*43	Encoder/decoder with pulse-code-modulation filters of C-MOS technology, capable of voice digitisation and reconstruction at a speed of 64 Kbits/s or more but not exceeding 2 048 Kbits/s, with a single power supply of 5 V, a power dissipation not exceeding 37 mW in operating mode and not exceeding 3 mW in power down mode, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 7508 B 7509 B or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*61	FM receiver of bipolar technology, capable of operating at an input frequency rage of 200 MHz, with an FM signal demodulating function, comprising at least 2 mixers, an oscillator, a diode and a Receive Signal Strengh Indicator (RSSI), in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 13135 MC 13136 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*67	FM-band receiver of BiMOS technology, comprising a compression circuit, a decompression circuit, 2 mixers, 2 phase-locked loop (PLL) circuits, an intermediate frequency (IF) amplifier, a receive signal strength indicator (RSSI), a serial interface circuit and a supply voltage detection circuit, in the form of a monolithic integrated mixed analogue-digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MC 13108 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 95	*69	Comparator of C–MOS technology, capable of voltage comparison, with a propagation delay of not more than 12 µs, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MAX 921 MAX 923 MAX 931 MAX 933 MAX 922 MAX 924 MAX 932 MAX 934 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*70	Circuit for connecting/disconnecting busses, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 89F6248 89F7000 TL2218 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*71	Audio and video signal processing circuit of bipolar technology, comprising a phase–locked loop (PLL) circuit, a FM signal detector, an intermediate frequency (IF) amplifier, a pre–amplifier, a radio frequency (RF) automatic gain control amplifier and a video signal amplifier, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LA 7577 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*72	Circuit for speed and angle position measurement, of C–MOS technology, comprising 4 amplifiers, a demodulator, a counter, a voltage inverter, a latch and a voltage controlled oscillator, in the form of a monolithic integrated mixed analoque–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: RDC 19220 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*73	Transmitter/receiver of C–MOS technology, providing line distortion equalization and data conversion, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TXC 07225 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*74	Demodulator of BiMOS technology, capable of processing encoded data from a magnetic stripe reader, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 95 *(cont'd)*		— an identification marking consisting of or including the following combination of figures and letters: M 56710FP or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 95	*75	Modulator of C–MOS technology, having a dynamic range of 123 dB in a bandwidth of 375 Hz or a dynamic range of 124 dB in a bandwidth of 500 Hz, in the form of a monolithic integrated mixed analogue–digital circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CS 5321 CS 5323 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*01	Filter of C–MOS technology, with a programmable cut–off frequency of 4,5 MHz or more but not exceeding 25,2 MHz and a programmable frequency amplification not exceeding 9 dB, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 896 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*02	Programmable filter of bipolar technology, with a programmable cut–off frequency of 5 MHz or more but not exceeding 15 MHz and a programmable peak frequency and bandwidth, comprising a seven–pole filter and a differentiator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32F8011 32F8012 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*03	Analogue signal microprocessor of bipolar technology, providing automatic gain control, read–signal processing and generation of head–positioning signals for magnetic heads in disc storage units, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SN 28961 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*08	Modulator of bipolar technology, operating in the UHF band, for the conversion of audio and video signals, in a frequency range of 470 MHz or more but not exceeding 630 MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ALP 101 CXA 1333 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*11	AM and FM receiver of bipolar technology, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1030 P CXA 1240 P or — other identification markings relating to devices complying with the abovementioned description	7
ex 8542 19 99	*13	AM or FM transmitter of bipolar technology, with AM or FM signal modulation function, comprising 3 amplifiers, a prescaler and 2 oscillators, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MC 13175 MC 13176 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*14	AM-band receiver of bipolar technology, providing conversion of radio frequency (RF) into dual intermediate frequency (IF) and detection of audio frequency, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 3848 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*15	FM-band receiver/demodulator of bipolar technology, comprising 2 conversion mixers, a data slicer and 6 amplifiers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1QX6 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*16	FM-band transmitter/receiver of bipolar technology, comprising 2 conversion mixers, a prescaler and 4 amplifiers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 1QX5 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*17	Switch unit of bipolar technology, for audio signals, having a distortion not exceeding 0,005 %, comprising 2 control units and 2 alternating switches, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TK 15022 Z or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*18	Switch unit of gallium arsenide (GaAs) semiconductor material, with an insertion loss not exceeding 1,6 dB at a frequency of 2 GHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: SW 239 SW 259 SW 419 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*21	Audio noise reduction circuit of bipolar technology, having an input voltage not exceeding 18 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LM 1894 TK 10654 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*23	Monolithic integrated analogue circuit of bipolar technology, for overvoltage protection, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures or of figures and letters: 1515 P2 P6 TISP 2180 P0 P3 TISP 1072F3 TISP 2290 P1 P4 TISP 1082 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*26	Frequency converter of gallium arsenide (GaAs) semiconductor material, for the conversion of frequencies of 10,25 GHz or more but not exceeding 12,75 GHz to frequencies of 950 MHz or more but not exceeding 2 050 MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 20070C AKD 12010 AKD 12575 AND 2001T4C AKD 1 2 000 AKD 12011 AKD 2 400 FMM 5103 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*27	Voltage-to-frequency converter, comprising an amplifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: VFC32 VFC100 VFC101 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*28	Frequency converter of bipolar technology, with a conversion gain of 7 dB, capable of converting an input frequency of 65,8 MHz into an output frequency of 800 kHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures: 806-0227 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*29	Current–to–voltage converter with an input current not exceeding 100 µA and an output voltage not exceeding –10 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: ACF 2101 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*31	Converter/amplifier of bipolar technology, with an output level of 22 dBm at a frequency of 900 MHz and an input level of –6 dBm, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: HP 3001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*34	RMS–converter for computing the root mean square (RMS) value of wave–forms and converting this value to an equivalent direct current or an equivalent direct voltage, in the form of an monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 536 A AD 636 AD 637 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*37	Temperature transducer, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AD 590 AD 592 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*38	Air pressure sensor, operating with a pressure range of 20 kPa to 105 kPa, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MPX 4100A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*41	Image sensor consisting of a row of not more than 5 000 photosensitive areas and a matrix linked to shift registers, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: LZ 2019 PD 3573 TCD 103 TCD 105 TCD 133 TCD 141 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*42	Interline charge-coupled (CCD) image sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ICX 018 ICX 022 ICX 038 PD 3732 ICX 021 ICX 024 ICX 039 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*44	Video processing circuit of bipolar technology, for signals from a charge-coupled (CCD) image sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: AN 2014S CXA 1390 IR 3P69 IR 3P97 AN 2145FHP CXA 1391 IR 3P81A IR 3Y17 CXA 1310AQ CXA 1392 IR 3P92 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*47	Signal processing circuit of C-MOS technology, providing delay of scanning periods for horizontal image lines of a charge-coupled (CCD) image sensor, comprising a clockgenerator, a clamp circuit and a sample and hold circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXL 1 506 MSM 6965 RS MSM 7401 RS or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*50	Detector for amplitude peaks in read/write signals of disc storage units, consisting of a differential amplifier with automatic gain control and a precision full-wave rectifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 32P3041 ML 8464 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*53	5-channel voltage comparator for monitoring lamp-circuits, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 22001 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*55	Voltage reference circuit providing a typical output voltage not exceeding 10 V with a drift slope (output voltage temperature co-efficient) not exceeding 25 ppm/°C, in the form of a monolithic integrated analogue circuit contained in a housing bearing:	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99 *(cont'd)*		— an identification marking consisting of or including one of the following combinations of figures and letters: AD 580 AD 680 LT 1021 REF 102 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*56	Voltage reference circuit with a reverse breakdown of 1,235 V (±4 mV) or 2,5 V (±20 mV), in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1004 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*58	Voltage converter and regulator of bipolar technology, with a voltage loss not exceeding 1,6 V at an output current of 100 mA, operating with a supply voltage range of 3,5 V or more but not exceeding 15 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LT 1054 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*59	Voltage converter of C-MOS technology, capable of inverting, doubling, dividing or multiplying input voltages, operating at a supply voltage range of 1,5 V or more but not exceeding 10 V, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ICL 7660 MAX 1044 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*60	Voltage-to-current converter of bipolar technology, with a selectable input voltage range and a power supply voltage of 13,5 V or more but not exceeding 40 V, comprising a current transmitter and a voltage reference circuit, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: XTR 110 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*61	Voltage converter of C-MOS technology, capable of transforming an input voltage level not exceeding 5 V at an input current not exceeding 0,1 µA into an output voltage not exceeding 15 V at an ouput current not exceeding 1 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: LR 36683N or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*63	Current transmitter of bipolar technology, with an output current of 4 mA or more but not exceeding 20 mA, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: XTR 103 XTR 104 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*68	Frequency converter of gallium arsenide (GaAs) semiconductor material, with a typical conversion gain of 20, 26 or 50 dB, capable of converting an input frequency of 500 MHz or more but not exceeding 2,5 GHz into an output frequency of 30 MHz or more but not exceeding 500 MHz, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TQ 9201 TQ 9202 TQ 9203 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*69	Frequency converter of gallium arsenide (GaAs) semiconductor material, with a conversion gain of 6 dB, capable of converting an input frequency of 950 MHz or more but not exceeding 2 050 MHz into an output frequency of 480 MHz, comprising an oscillator, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: ADC 20013 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*70	Frequency converter of bipolar technology, operating with a frequency range of 800 MHz to 900 MHz and with an input level not exceeding –6 dBm, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1851N or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*73	6–channel DC–to–DC converter of BiMOS technology, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MB 3799 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*75	Amplifier/comparator of bipolar technology, for the amplification and comparison of phase/frequency signals from sensor inputs, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: CXA 1418 N or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*76	Voltage detection circuit, capable of resetting external circuits, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: M 51957 M 51958 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*78	Half-bridge rectifier, consisting of 2 field effect transistors of MOS technology (MOSFETs), capable of driving inductive or capacitive loads with a nominal voltage of 50 V and a nominal current of 2 A, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: Si9950DY or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*82	Programmable diode array, consisting of 14 individual diodes and a rectifier, of gallium arsenide (GaAs) semiconductor material, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 16G010 16G011 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*84	Phase-locked loop (PLL) demodulator, with a typical operating frequency of 480 MHz, comprising an oscillator and a carrier detector, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TDA 8012M or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*88	Acceleration measurement circuit, comprising a capacitif sensor, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: ADXL50 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*89	Photodetector, operating at a wavelength of 780 nm, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: CXA 1753 M 52104 PHD 003 PN 7611 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 19 99	*90	Mixer/oscillator, with a frequency range of 48 MHz or more but not exceeding 860 MHz, comprising a frequency bandswitch and an intermediate frequency (IF)-amplifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: TDA 5330 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*91	Filter network only consisting of 16 resistors, 16 capacitors and 16 diodes, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: USRC 1002 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 19 99	*92	Isolation circuit for error signals, comprising an amplitude modulator and an amplifier, in the form of a monolithic integrated analogue circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: UC 1901 UC 2901 UC 3901 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 10	*10	Microprocessor of C-MOS technology, with a processing capacity of 32 bits, consisting of a single substrate layer on which are mounted 2 chips, one comprising a central processing unit (CPU) and the other a memory unit, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures: 57-00000 57-19400 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 30	*30	Four-channel digital-to-analogue converter, each channel having a capacity of 12 bits, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: AD 390 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 30	*40	16-bit digital-to-analogue converter, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DAC 705 DAC 706 DAC 707 DAC 708 DAC 709 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 30	*50	12-bit analogue-to-digital converter of C-MOS technology, comprising a sample and hold amplifier having a dynamic performance of 1 MHz per second or more, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ADS 112 ADS 117 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*10	Amplifier for the frequency range 20 Hz to 20 000 Hz, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: STK 4041 STK 4151 STK 4201 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*15	Amplifier of gallium arsenide (GaAs) semiconductor material, operating within a frequency range of 872 MHz to 905 MHz, with an output power not exceeding 1,259 W (31 dBm) and an input power not exceeding 0,01 W (10 dBm), in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: 5PG612301 FMC 080901-60 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*20	Amplifier for a nominal range of 0 or more but not exceeding 70 kHz, with an isolation voltage of 750 V or more and a leakage of not more than 1 μA, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: ISO 100 ISO 102 ISO 106 ISO 120 ISO 121 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*30	Amplifier with a programmable gain factor, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 3606 G or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*50	Amplifier, operating with a supply voltage of 28 V, for frequencies of 1 625 MHz or more but not excedding 1 645 MHz, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: STM 1 645-30 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 50	*70	Amplifier of bipolar technology, operating within a frequency range of 800 MHz to 950 MHz, with at least one of the following characteristics: a) an output power of 12,5 W at an input power of 100 mW, b) an output power of 20 W at an input power of 200 mW, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) PHW 5113 b) MHW 820–1 b) MHW 820–2 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 50	*80	Amplifier, operating within a frequency range of 68 MHz or more but not exceeding 470 MHz, with an output power not exceeding 40 W and an input power of 150 mW or more, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: BGY 135 BGY 145 BGY 45 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*15	Dual ceramic filter, operating within a frequency range of 872 MHz to 950 MHz, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: 7FG6314B or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*25	Current detector, having an input resistance not exceeding 9 Ohm, withstanding an isolation AC voltage of 3,75 kV or 4 kV during 1 minute, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: HFS 113F001A1 MA 91 000018 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*30	Voltage regulator with an input voltage not exceeding 1 kV and a fixed output voltage of 41,8 V (±0,5 V), 102,6 V (±1 V) or 124,3 V (±1 V), in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: STR 51402 STR 51424 STR 54041 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*50	Voltage and current regulator, having an output voltage not exceeding 850 V at a drive current not exceeding 0,7 A, comprising a power transistor and a control circuit with an oscillator, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: S 5706 S 6708 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8542 20 80	*60	Voltage regulator with a nominal input operating voltage of 276 V, an input current not exceeding 8 A and an operating frequency not exceeding 200 kHz, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MA 2810 MA 2820 MA 2830 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 20 80	*70	Voltage and current regulator, having an input voltage not exceeding 35 V and a quiescent current not exceeding 100 µA, comprising a field-effect transistor (FET) with a drain current not exceeding 32 A, in the form of a hybrid integrated circuit contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: STR M6523 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 80 00	*03	Silicon power bridge rectifier, with reverse voltage not exceeding 800 V and an average forward current of 1 A, in the form of a microassembly contained in a housing	0
ex 8542 80 00	*04	Dual silicon zener diode, with a zener voltage of 11 V or more but not exceeding 13 V and a dissipation rate not exceeding 200 mW, in the form of a microassembly contained in a housing	0
ex 8542 80 00	*05	Quintuple field-effect transistor (FET), having a drain-to-source breakdown-voltage of 100 V or more, operating with a continuous drain current not exceeding 5 A, and with a dissipation rate not exceeding 35 W, in the form of a microassembly contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: SLA 5021 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8542 80 00	*07	Silicon diode assembly, comprising a diode with a reverse recovery time not exceeding 1,5 µs, a recurrent peak reverse voltage not exceeding 1 500 V and an average forward current not exceeding 5 A, in the form of a microassembly contained in a housing	0
ex 8543 80 95	*21	Amplifier of bipolar technology, operating within a frequency range of 68 MHz to 88 MHz, with an output power of 5 W at an input power of 1 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MHW 105 XHW 105 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8543 80 95	*25	Amplifier of gallium arsenide (GaAs) semiconductor material, operating with a frequency range of 1 710 MHz to 1 785 MHz, with an output power of 3 W at an input power of 1 mW or with an input level not exceeding 5 dBm and an output level of 30,8 dBm or more, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: FAO 1314 FMC 1717 PHW 9012 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*27	Amplifier of bipolar technology, operating within a frequency range of 136 MHz to 174 MHz, with an output power of 7 W at an input power of 1 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MHW 607 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*28	Amplifier of bipolar technology, operating within a frequency range of 400 MHz to 470 MHz, with at least one of the following characteristics: a) an output power of 3 W at an input power of 1 mW, b) an output power of 7 W at an input power of 1 mW, c) an output power of 20 W at an input power of 150 mW, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: a) MHW 704 b) MHW 707 c) MHW 720 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*29	Amplifier with an isolation voltage of 1 500 V or more and with a leakage current not exceeding 0,5 µA, consisting of 2 capacitors and 2 monolithic integrated circuits on a printed circuit which is mounted on a plastic carrier, the whole contained in a housing the exterior dimensions of which do not exceed 8 x 21 mm, with not more than 8 connections and bearing: — an identification marking consisting of or including the following combination of figures and letters: ISO 122 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*31	Electromagnetic display consisting of 7 electromagnetic coils, which by means of the residual magnetism in the stators provide that the last indication remains available (set state), and 7 pivoting light-reflecting segments each of which is attached to a bar magnet. The display is contained in a housing the exterior dimensions of which do not exceed 28 x 36 x 50 mm	0
ex 8543 80 95	*61	Speech reproducing module, consisting of a printed circuit on which are mounted: — a synthesiser, in the form of a monolithic integrated circuit contained in a housing, — a piëzo-electric or magnetic loudspeaker, — two or three supply batteries, a switch, three capacitors,	

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8543 80 95 *(cont'd)*		and whether or not incorporating: — either a read only memory, non-programmable (ROM) with a storage capacity of 1 Mbit or an UV erasable, programmable, read only memory (EPROM) with a storage capacity of 128 Kbits, in the form of a monolithic integrated circuit and contained in a housing, the whole contained in a housing the exterior dimensions of which do not exceed 4 x 60 x 95 mm, with not more than 24 connections and bearing: — an identification marking consisting of or including the following combination of letters: SPMS or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*71	Digital image processor unit with a speed of 1 to 50 images per second, with a spatial resolution of 512 x 512 pixels or more and a radiometric resolution of 16 bits, comprising supply units and 11 printed circuits on which are mounted integrated circuits and other active and passive elements, the whole mounted in a frame, for use in the manufacture of cardiodiagnostic appparatus (a)	0
ex 8543 80 95	*72	Radio frequency (RF) modulator, operating with a frequency range of 43 MHz or more but not exceeding 870 MHz, capable of switching VHF and UHF signals, consisting of active and passive elements mounted on a printed circuit, contained in a housing	0
ex 8543 80 95	*73	Amplifier, operating with a frequency range of 925 MHz to 960 MHz, with an output power of 16 W at an input power of 0,035 W (15,5 dBm), consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: MHW 916 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*74	Voltage controlled oscillator (VCO), for frequencies of 373 MHz or more but not exceeding 1 038 MHz and operating at a supply voltage of 4,2 V (±0,4 V), comprising a quartz resonator mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: MQC403 MQC404 MQE001 MQE201 URAB8 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*75	Temperature compensating frequency oscillator with a frequency of 12,8 or 15,15 MHz and operating at a supply voltage of 5 V (±0,3 V), comprising a printed circuit on which are mounted at least a piezo-electric crystal and an adjustable capacitor, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: TCO 903 TCO 937 TXS 1144 or — other identification markings relating to devices complying with the abovementioned description	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 8543 80 95	*76	Voltage controlled oscillator (VCO), with a frequency not exceeding 2 038 MHz at a controlled supply voltage of 1 V and a frequency of 2 126 MHz or more at a controlled supply voltage of 4 V, consisting of aktive and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: URAE8X956A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8543 80 95	*77	Voltage controlled oscillator (VCO), with a frequency not exceeding 1 181 MHz at a controlled supply voltage of 1 V and a frequency of 1 243 MHz or more at a controlled supply voltage of 4 V, consisting of active and passive elements mounted on a printed circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: URAE8X960A or — other identification markings relating to devices complying with the abovementioned description	0
ex 8544 60 10	*10	Electric cable, insulated with plastic, for a voltage of 28 kV or more but not exceeding 32 kV, fitted with an anode in a rubber cap at one end and with a metallic connector at the other end	0
ex 8545 90 90	*01	Cell and battery carbon, in the form of rods, with a length of 34 mm or more but not exceeding 160 mm and a diameter not exceeding 12 mm	0
ex 8548 00 00	*92	Contact image sensor	0
ex 8548 00 00	*94	Optical unit, consisting of a laserdiode and a photodiode, operating at a typical wavelength of 635 or 670 nm	0
ex 8548 00 00	*95	Infrared signal receiver unit, consisting of a photodiode and at least an amplifier in the form of a monolithic integrated circuit, contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: GP1U58XB SBX 1610 or — other identification markings relating to devices complying with the abovementioned description	0
ex 8548 00 00	*96	Fibre-optic cable transmitter unit, consisting of a light-emitting diode (LED) operating at a wavelength of 660 nm (±30 nm) and a control circuit in the form of a monolithic integrated circuit, contained in a housing bearing: — an identification marking consisting of or including the following combination of figures and letters: GP1F32T or — other identification markings relating to devices complying with the abovementioned description	0
ex 9001 10 10 ex 9001 10 90	*10 *10	Image reverser made up from an assembly of optical fibres	0
ex 9001 20 00	*10	Material consisting of a polarising film, supported on one or both sides by transparent material	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 9001 20 00	*20	Polarising plastic film, consisting of a transparent protective film and a polarising membrane	0
ex 9001 20 00	*30	Polarising lenticular plastic plate with a lenticular array pitch of 0,78 mm, a thickness not exceeding 0,99 mm and the exterior dimensions of which do not exceed 740 x 974 mm	0
ex 9001 90 90	*40	Optical fibre plate, for use in the manufacture of screens and photocathodes for image intensifiers (a)	0
ex 9001 90 90	*60	Prism for the splitting of light, unmounted, for use in the manufacture of charged-coupled (CCD) cameras (a)	0
ex 9002 11 00	*10	Adjustable lens unit, having a focal length of 90 mm or more but not exceeding 180 mm and comprising a combination of between 4 and 8 glass or methacrylic lenses with a diameter of 120 mm or more but not exceeding 180 mm, each lens coated on at least one side with a magnesium fluoride layer, for use in the manufacture of video projectors (a)	0
ex 9002 11 00	*50	Lens unit, having a focal length of 75 mm or more but not exceeding 94 mm, consisting of glass or plastic lenses, with a diameter of 60 mm or more but not exceeding 180 mm	0
ex 9002 19 00	*10	Lens unit, having a focal length of 24,96 mm (±0,1 mm), a diameter of 16 mm and a length of 16 mm, for use in the manufacture of products falling within subheading 8517 82 10 (a)	0
ex 9002 90 91	*10	Optical element comprising an octagonal Fresnel lens, for use in the manufacture of overhead projectors (a)	0
ex 9002 90 91	*20	Lens, mounted, having a fixed focal length of 3,8 mm (±0,19 mm) or 8 mm (±0,4 mm), with a relative aperture of F2.0 and a diameter not exceeding 33 mm, for use in the manufacture of charged-coupled (CCD) cameras (a)	0
ex 9013 80 90	*10	Polarisation insensitive fibre-optic isolator, operating at a wavelength of 1 300, 1 480 or 1 550 nm, contained in a cylindrical housing with 2 optical fibre cables	0
ex 9017 90 00	*10	Thermal printer head, comprising at least 7 168 heater elements mounted on 2 or more ceramic supports, the whole contained in a housing the exterior dimensions of which exceed 21 x 39 x 639 mm	0
ex 9021 30 90	*21	Vascular protheses, neither woven nor knitted, of which the largest opening has an internal diameter not exceeding 6 mm	2,5
ex 9021 30 90	*29	Vascular prothesis, neither woven nor knitted, of which the largest opening has an internal diameter of more than 6 mm but not exceeding 8 mm	0
ex 9021 30 90	*30	Heart valves and parts thereof	0
ex 9110 12 00	*91	Assembly consisting of a printed circuit on which are mounted one quartz oscillator, at least one watch circuit and, whether or not integrated, at least one capacitor, of a thickness not exceeding 5 mm, for use in the manufacture of products falling within Chapter 91 (a)	0

CN code	TARIC	Description	Rate of autonomous duty (%)
ex 9110 90 00 ex 9114 90 00	*92 *91	Assembly consisting of a printed circuit on which is mounted a watch circuit or a watch circuit and a quartz oscillator, of a thickness not exceeding 5 mm, for use in the manufacture of products falling within Chapter 91 (a)	0
ex 9110 90 00	*93	Assembly consisting of a printed circuit on which is mounted at least one watch circuit, a quartz oscillator and a piezo-electric sound element, with a thickness exceeding 5 mm, for the manufacture of products falling within Chapter 91 (a)	0
ex 9110 90 00	*94	Clock/calendar circuit, consisting of a printed circuit on which are mounted at least a quartz oscillator and a monolithic integrated circuit, the whole contained in a housing bearing: — an identification marking consisting of or including one of the following combinations of figures and letters: DS 1287 DS 1387 MK 48T08 MK 48T18 RTC 65271 DS 12887A MK 48T02 MK 48T12 RTC 63421 RTC 72423 or — other identification markings relating to devices complying with the abovementioned description	0
ex 9608 91 00	*10	Non-fibrous plastic pen-tips with an internal channel	0
ex 9613 90 00	*20	Piezo-electric ignition mechanism	0

(a) Control of the use for this special purpose shall be carried out pursuant to the relevant Community provisions.